



SPECIFICATIONS OF LCD MODULE

GDM12864A is a dot matrix graphic LCD module, which is fabricated by low power COMS technology. It can display 128*64 dots size LCD panel using a 128*64 bit-mapped Display Data RAM (DDRAM). It interfaces with an 8-bit microprocessor.

1.Features

Display format: 128*64 dots matrix graphic

• Easy interface with 8-bit MPU

Low power consumption

• STN (Blue) ,Negative Image ,Transmissive

Viewing angle: 6 O'clock

Driving method: 1/64 duty, 1/9 bias

· LED Sidelight: color White

Connector: Zebra

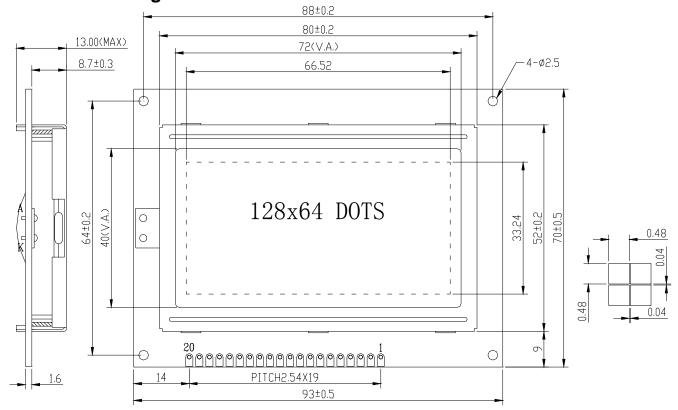
2.Mechanical Specifications

Item	Dimension	Unit
Viewing Area (W*H)	72.0*40.0	mm
Number of Dots	128.0*64.0	PCS
Dot Size (W*H)	0.48*0.48	mm
Dot Pitch (W*H)	0.52*0.52	mm
Module Size With B/L	93.0*70.0*13.0	mm

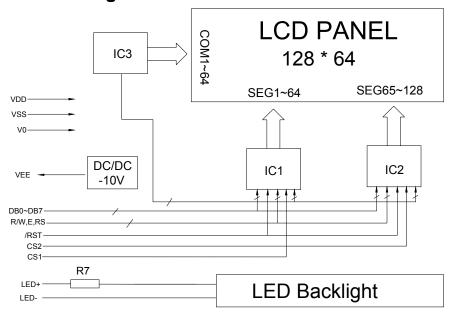
3. Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	
Power Voltage	V _{DD} -V _{SS}	0	5.5	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Input Voltage	Vı	Vss	V_{DD}	V	
Operating Temperature Range	V _{OP}	-20	+70	င	
Storage Temperature Range	Tst	-25	+75	C	

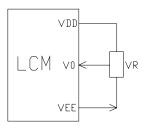
4. Mechanical diagram



5.Block diagram



Contrast adjust

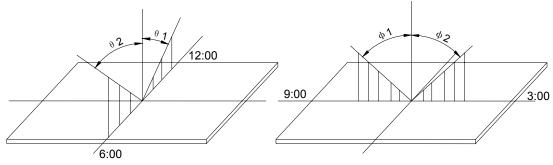


For Module with Normal Temperature Range Fluid V_{DD~}V₀: LCD Driving voltage VR: 10k~20k

6.Description Of Terminals

Pin No.	Pin Name	Input/ Output	Function						
1	VSS	_	VSS: GND						
2	VDD	_	VDD: +5V						
3	V0	_	With VEE pin to adjust LCD contrast.						
4	RS	Input	Register selection RS Description RS=1 The data in DB [7:0] is display data. RS=0 The data in DB [7:0] is control data						
5	R/W	Input	Read or Write RW Description RW=1 Data appears at DB[7:0] and can be read by the CPU RW=0 Display data DB[7:0] can be written						
6	Е	Input	Enable signal						
7~14	DB0~7	I/o	Data bus [0~7] Bi-directional data bus						
15	CS1	Input	Chip selection When CS1=H, CS2=L, select IC1						
16	CS2	Input	Chip selection When CS1=L, CS2=H, select IC2						
17	/RST	Input	Reset signal. When RST=L 1. ON/OFF register becomes set by 0.(display off) 2. Display start line register becomes set by 0 (Z-address 0 set, display from line 0) After releasing reset, this condition can be changed only by Instruction.						
18	VEE	Input	Negative voltage output For LCD driver circuit (-10.0V)						
19	LED+	LED BKL POWER	Power supply for BKL (+5.0V)						
20	LED-	SUPPLY	Power supply for BKL (GND)						

7. Optical Characteristics

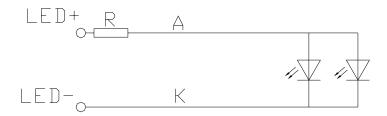


STN type display module (Ta=25°C, VDD=5.0V)

	- (/					
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	
	θ 1			20			
Viowing angle	θ 2	Cr≥3		40		deg	
Viewing angle	Ф1	Ur≥3		35			
	Ф2			35			
Contrast ratio	Cr		-	10	-	-	
Response time (rise)	Tr	-	-	200	250	me	
Response time (fall)	Tr	_	_	300	350	ms	

8. Electrical Characteristics

> Backlight circuit diagram(light 2X 1,Colour: White)



LED ratings

(voltage= 5V,R= 100Ω)

				<u> </u>	
Item	Symbol	Min	Тур.	Max	Unit
Forward Voltage	V _{AK}	2.8	3	3.2	V
Forward current	IF	-	20	30	mA
Power	Р			90	mW
Peak wave length	λ p				nm
Luminance	Lv		80		Cd/m ²

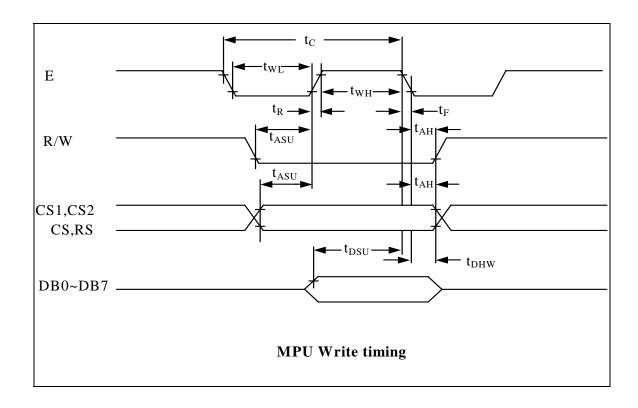
> DC Characteristics

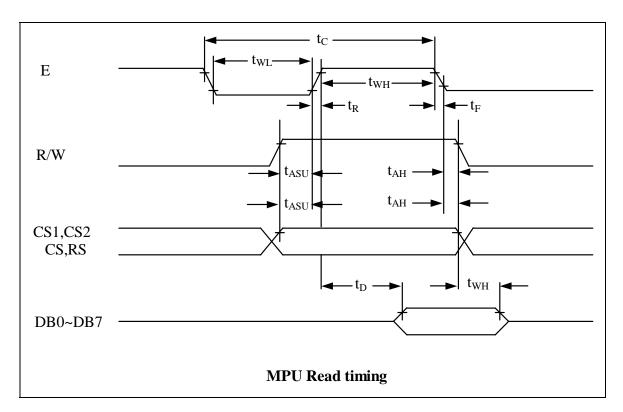
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage for LCD	VDD-V0	Ta =25℃	-	9.5	-	V
Input voltage	VDD		4.7	5.0	5.5	V
Backlight supply voltage	VF		-	3.0		
Supply current	IDD	Ta=25℃, VDD=5.0V	-	2	3.5	mΛ
Backlight supply current	klight supply current IF VI		-	20		mA
Input leakage current	ILKG		-	-	1.0	uA
"H" level input voltage	VIH		2.2	-	VDD	
"L" level input voltage	VIL	Twice initial value or less	0	-	0.6	V
"H" level output voltage	VOH	LOH=-0.25mA	2.4	-	-	
"L" level output voltage	VOL	LOH=1.6mA	-	-	0.4	

> AC. Characteristics

VDD=5V, Ta=25°C

Characteristic	Symbol	Min	Тур	Max	Units
E Cycle	tc	1000	-	-	
E High Level Width	twн	450	ı	-	
E Low Level Width	tw∟	450	-	-	
E Rise Time	tr	-	-	25	
E Fall Time	t _F	ı	ı	25	
Address Set-Up Time	t _{ASU}	140	-	-	ns
Address Hold Time	t ah	10	1	-	
Data Set-Up Time	tsu	200	ı	-	
Data Delay Time	t₀	ı	ı	320	
Data Hold Time (Write)	tohw	10	-	-	
Data Hold Time (Read)	t DHR	20	-	-	





9. OPERATING PRINCIPLES & METHODS

Output register

Output register stores the data temporarily from display data RAM when CS1, CS2 are in active mode and R/W and RS=H, stored data in display data RAM is latched in output register. When CS1 to CS2 is in active mode and R/W=H, RS=L, status data (busy check) can read out.

To read the contents of display data RAM, twice access of read instruction is needed. In first access, data in display data RAM is latched into output register. In second access, MPU can read data, which is latched. That is to read the data in display data RAM, it needs dummy read. But status read is not needed dummy read.

RS	R/W	Function							
	L	Instruction							
-	Н	Status read (busy check)							
Н	L	Data write (from input register to display data RAM)							
П	Н	Data read (from display data RAM to output register)							

> Reset

The system can be initialized by setting RST terminal at low level when turning power on, receiving instruction from MPU. When RST becomes low, following procedure is occurred.

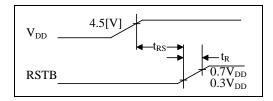
- 1. Display off
- 2. Display start line register become set by 0.(Z-address 0)

While RSTB is low, No instruction except status read can by accepted. Therefore, execute other instructions after making sure that DB4= (clear RST) and DB7=0 (ready) by status read instruction.

The conditions of power supply at initial power up are shown in table 1.

Table 1. Power Supply Initial Conditions

Item	Symbol	Min	Тур	Max	Unit
Reset Time	trs	1.0	-	-	us
Rise Time	t_R	-	-	200	ns

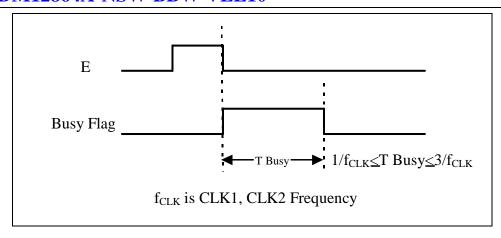


Busy flag

Busy flag indicates that NT7108 is operating or no operating. When busy flag is high, NT7108 is in internal operating.

When busy flag is low, NT7108 can accept the data or instruction.

DB7indicates busy flag of the NT7108.



Display On/Off Flip-Flop

The display on/off flip-flop makes on/off the liquid crystal display. When flip-flop is reset (logical low), selective voltage or non-selective voltage appears on segment output terminals. When flip-flop is set (logic high), non selective voltage appears on segment output terminals regardless of display RAM data.

The display on/off flip-flop can changes status by instruction. The display data at all segments disappear while RSTB is low.

The status of the flip-flop is output to DB5 by status read instruction.

The display on/off flip-flop synchronized by CL signal.

X Page Register

X page register designates pages of the internal display data RAM. Count function is not available. An address is set by instruction.

> Y address counter

Y address counter designates address of the internal display data RAM. An address is set by instruction and is increased by 1 automatically by read or write operations of display data.

Display Data RAM

Display data RAM stores a display data for liquid crystal display. To indicate on state dot matrix of liquid crystal display, write datra1. The other way, off state, writes 0.

Display Start Line Register

The display start line register indicates of display data RAM to display top line of liquid crystal display. Bit data (DB<0.5>) of the display start line set instruction is latched in display start line register. Latched data is transferred to the Z address counter while FRM is high, presetting the Z address counter. It is used for scrolling of the liquid crystal display screen.

10.Display Control Instruction

The display control instructions control the internal state of the NT7108. Instruction is received from MPU to NT7108 for the display control. The following table shows various instructions.

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Read Display Date	1	1				Read	l data				Reads data (DB[7:0]) from display data RAM to the data bus.
Write Display Date	1	0				Write	e data		Writes data (DB[7:0]) into the DDRAM. After writing instruction, Y address is incriminated by 1 automatically		
Status Read	0	1	Bus y	0	ON /OF set 0 0 0 0 0				Reads the internal status BUSY 0: Ready 1: In operation ON/OFF 0: Display ON 1: Display OFF RESET 0: Normal 1: Reset		
Set Address (Y address)	0	0	0	1		Υ	addres	ss (0~6	3)		Sets the Y address at the column address counter
Set Display Start Line	0	0	1	1		Displa	ay star	t line ((0~63)		Indicates the Display Data RAM displayed at the top of the screen.
Set Address (X address)	0	0	1	0	1	1 1 1 Page (0~7)			ge (0~	Sets the X address at the X address register.	
Display On/off	0	0	0	0	1	1 1 1 1 0/1		Controls the display ON or OFF. The internal status and the DDRAM data is not affected. 0: OFF, 1: ON			

Display On/Off

Ī	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
	0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains in the display data RAM.

Therefore, you can make it appear by changing D=0 into D=1.

Set Address (Y Address)

 		- (/					
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0~AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write

operations of display data.

Set Page (X Address)

X address (AC0~AC2) of the display data RAM is set in the X address register.

Writing or reading to or from MPU is executed in this specified page until the next page is set.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

Display Start Line (Z Address)

Z address (AC0~AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others (1/32~1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Status Read

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted.

When BUSY is 0, the Chip is ready to accept any instructions.

ON/OFF

When ON/OFF is 1, the display is on.

When ON/OFF is 0, the display is off.

RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in the usual operation condition.

Write Display Data

Writes data (D0~D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Read Display Data

Reads data (D0~D7) from the display data RAM.

After reading instruction, Y address is increased by 1 automatically.

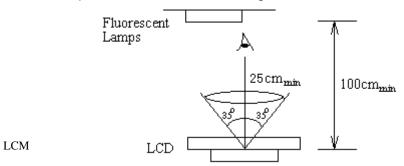
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

QUALITY SPECIFICATIONS

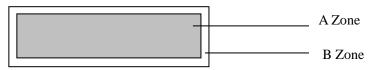
11.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 25 cm or more.

Viewing direction for inspection is 35° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).

B Zone: Non-active display area (outside viewing area).

11.2 Specification of quality assurance

AQL inspection standard

Sampling method: GB2828-87, Level II, single sampling

Defect classification (Note: * is not including)

Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
-		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

Note on defect classification

No.	Item		Criterion	
1	Short or open circuit		Not allow	
	LC leakage			
	Flickering			
	No display			
	Wrong viewing direction			
	Wrong Back-light			
2	Contrast defect	Re	efer to approval sam	ple
	Backgroundcolor deviation			
3	Point defect, Black spot, dust	<u></u>	Point Size	Acceptable Qty.
	(including Polarizer)	X	φ <u><</u> 0.10	Disregard 2
			0.10<φ≤0.15 0.15<φ≤0.25	1
	$\phi = (X+Y)/2$		φ>0.25	0
4	Line defect,	J	Unit: Inch	2
-			Line	Acceptable Qty.
	Scratch	<u> </u>	L W 0.05>W 3.0>L 0.1>W>0.05 2.0>L 0.15≥W>0.1	Disregard
				Unit: mm
5	Rainbow	Not more than two	color changes acros	ss the viewing area.

No	Item	Criterion
6	Chip Remark: X: Length direction Y: Short direction	Acceptable criterion $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
	Z: Thickness direction t: Glass thickness W: Terminal Width L:Glass length	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
		Acceptable criterion $\begin{array}{c cccc} X & Y & Z \\ \hline \leqslant 3 & \leqslant 2 & \leqslant t \\ \hline \text{shall not reach to ITO} \end{array}$
		Acceptable criterion $\begin{array}{c ccccccccccccccccccccccccccccccccccc$
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

No.	Item	Criterion
7	Segment pattern W = Segment width φ = (X+Y)/2	(1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ $\begin{array}{c ccccc} X & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & & & \\ Y & & & & & \\ Y & & & & \\ Y & & & \\ Y & & & & \\ Y & & & & \\ Y & $
8	Back-light	(1) The color of backlight should correspond its specification.(2) Not allow flickering
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Lead Land 50% lead
10	Wire	 (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.
11*	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.

No	Item	Criterion
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$
13	TAB	1. Position $\begin{array}{cccccccccccccccccccccccccccccccccccc$
		TAB bonding strength test TAB P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.

11.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	48	
High temp. Operating	70°C	48	No abnormalities
Low temp. Storage	-30°C	48	in functions
Low temp. Operating	-20°C	48	and appearance
Humidity	40°C/ 90%RH	48	
Temp. Cycle	0° C ← 25° C → 50° C (30 min ← 5 min → 30min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not make any modification on the PCB without consulting XIAMEM OCULAR
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.

- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C+10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

XIAMEM OCULAR LCDs and modules are not consumer products, but may be incorporated by XIAMEM OCULAR 's customers into consumer products or components thereof, XIAMEM OCULAR does not warrant that its LCDs and components are fit for any such particular purpose.

- The liability of XIAMEM OCULAR is limited to repair or replacement on the terms set forth below. XIAMEM OCULAR will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between XIAMEM OCULAR and the customer, XIAMEM OCULAR will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with XIAMEM OCULAR general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.