ShinDengen

KD3FB60

TRIACs
600V, 3A

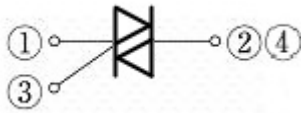
Feature

- Small SMD
- High voltage
- Tj=150℃
- Stable surge-on current capability
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FB
Package (JEDEC Code): TO-252AA

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	℃
Junction temperature	Tj		-40 to 150	℃
Repetitive peak off-state voltage	V_{DRM}		600	V
Non-repetitive peak off-state voltage	V_{DSM}	※	720	V
R.M.S. on-state current	$I_{T(RMS)}$	Tc=133℃, commercial frequency, sine wave, θ=360℃	3	A
Surge on-state current	I_{TSM}	Tj=25℃, 60Hz sine wave, Non-repetive 1 cycle peak ※	30	A
Current squared time	I^2t	Tj=25℃, t=8.33ms, Non-repetitive	3.7	A^2S
Critical rate of rise of on-state current	di/dt		50	A/μs
Peak gate dissipation	P_{GM}	f=60Hz, Duty≦10%	1.8	W
Average gate dissipation	$P_G(AV)$		0.3	W
Peak gate current	I_{GM}	f=60Hz, Duty≦10%	0.3	A
Peak gate voltage	V_{GM}		6	V

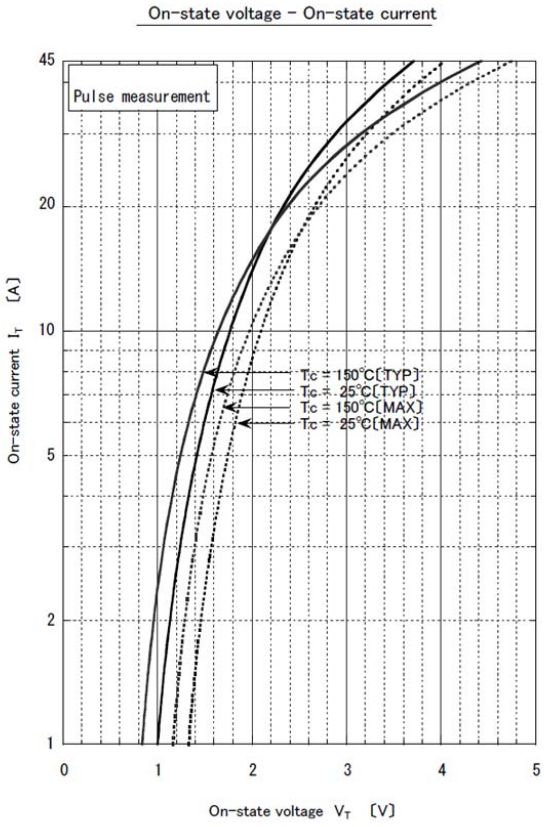
※ ：See the original Specifications

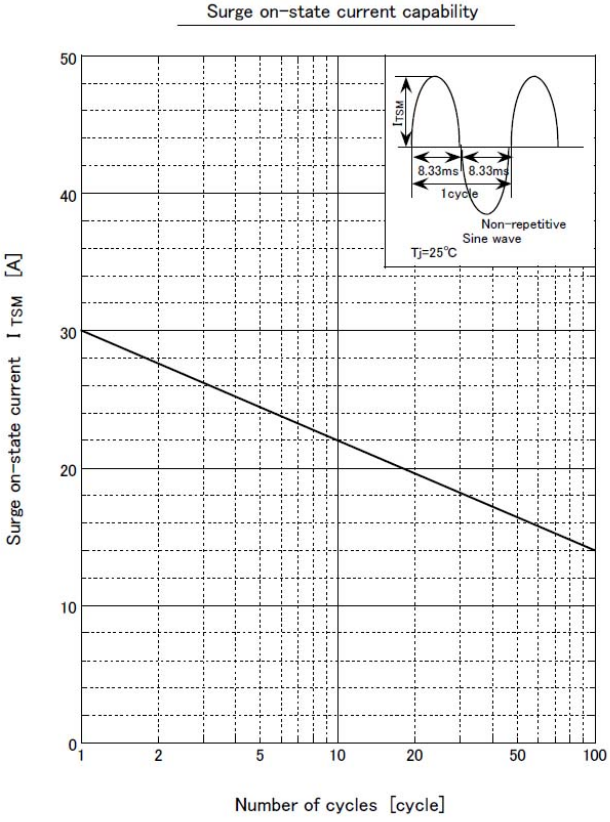
Electrical Characteristics (unless otherwise specified : Tc=25℃)

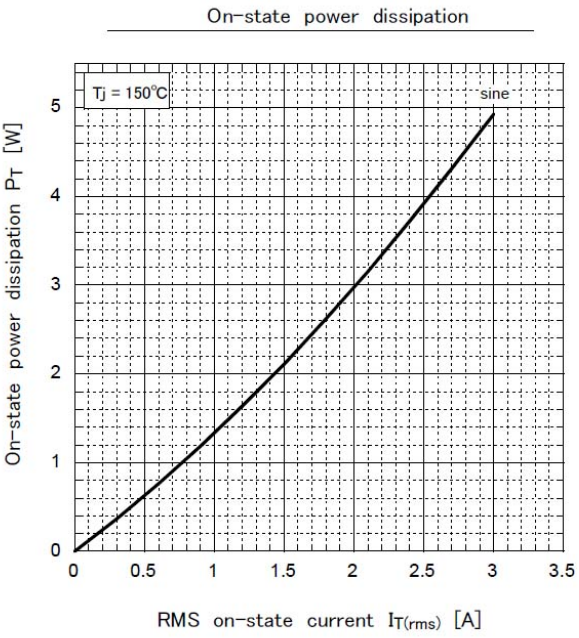
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Off-state current	I_{DRM}	VD=600V, Pulse measurement			10	μA
On-state voltage	V_{TM}	ITM=4.5A, Pulse measurement			1.7	V
Gate trigger voltage	V_{GTI}	VD=6V, RL=10Ω, T1-, T2+, G+			1.5	V
Gate trigger voltage	V_{GTII}	VD=6V, RL=10Ω, T1-, T2+, G-			1.5	V
Gate trigger voltage	V_{GTIII}	VD=6V, RL=10Ω, T1+, T2-, G-			1.5	V
Gate trigger voltage	V_{GTIV}	VD=6V, RL=10Ω, T1+, T2-, G+			- ※	V
Gate non-trigger voltage	V_{GD}	Tj=150℃, VD=1/2VDRM	0.1			V
Gate trigger current	I_{GTI}	VD=6V, RL=10Ω, T1-, T2+, G+			15	mA
Gate trigger current	I_{GTII}	VD=6V, RL=10Ω, T1-, T2+, G-			15	mA
Gate trigger current	I_{GTIII}	VD=6V, RL=10Ω, T1+, T2-, G-			15	mA
Gate trigger current	I_{GTIV}	VD=6V, RL=10Ω, T1+, T2-, G+			- ※	mA
Latching current	I_{LI}	IG=0.1A, T1-, T2+, G+			100	mA
Latching current	I_{LII}	IG=0.1A, T1-, T2+, G-			100	mA
Latching current	I_{LIII}	IG=0.1A, T1+, T2-, G-			100	mA
Latching current	I_{LIV}	IG=0.1A, T1+, T2-, G+			- ※	mA
Holding current	I_H	ITM=1A			100	mA
Critical rate of rise of commutating voltage	(dv/dt)c	Tj=150℃, VD=2/3VDRM, (di/dt)c=-1.5A/ms	1			V/μs
Thermal resistance	Rth(j-c)	Junction to case with heatsink			3	℃/W

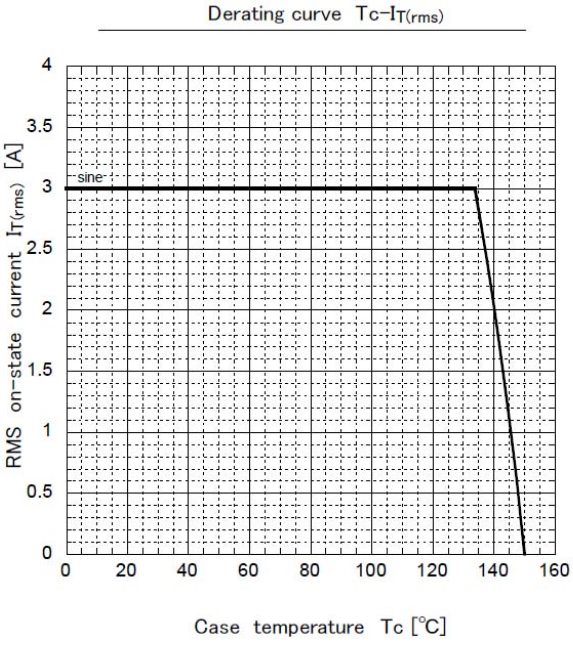
※ :See the original Specifications

CHARACTERISTIC DIAGRAMS

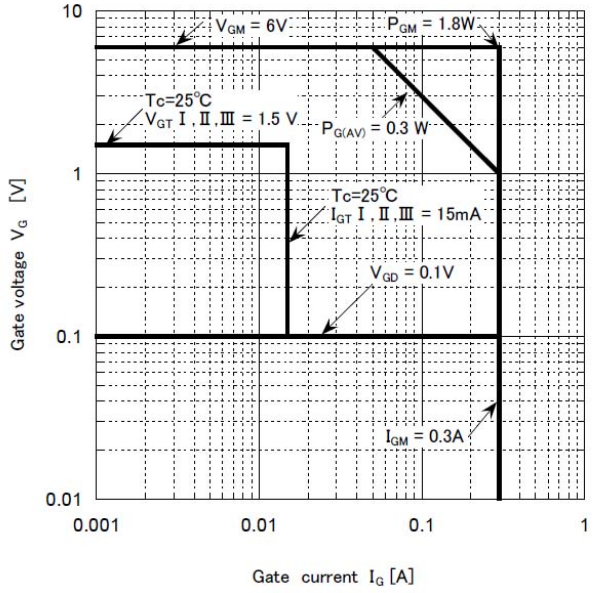




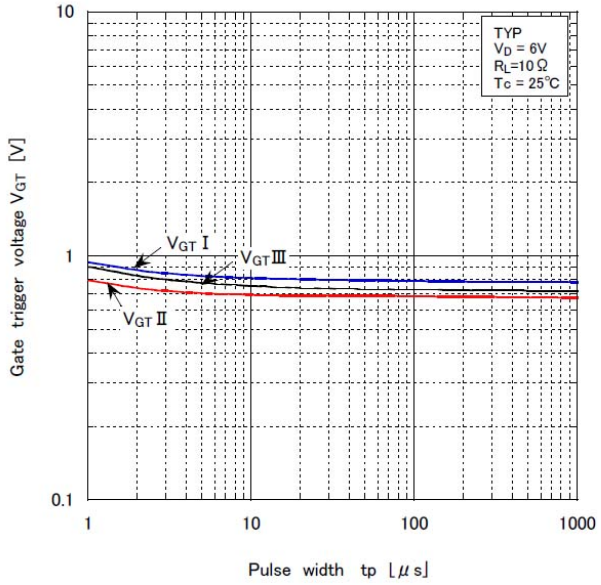




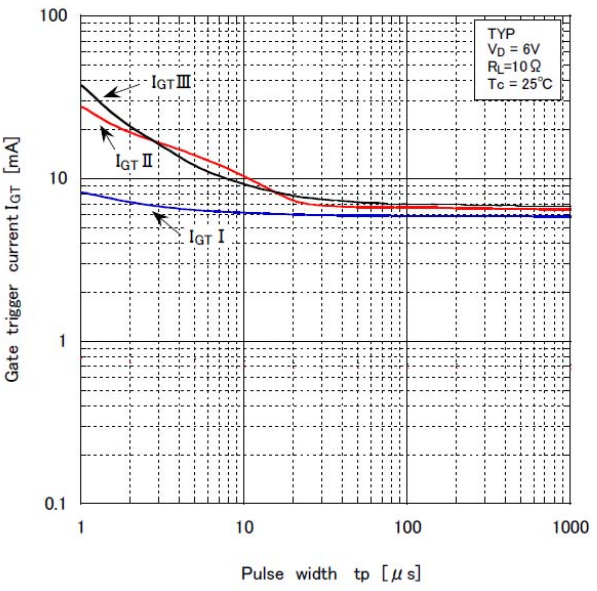
Gate characteristic



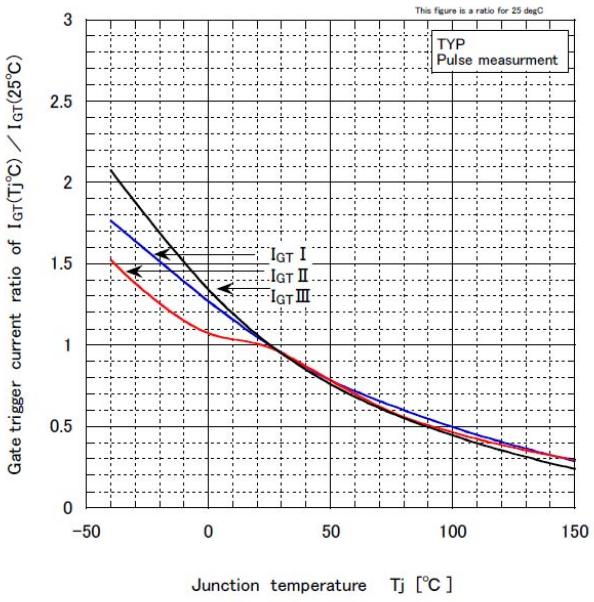
Gate trigger voltage vs Pulse width



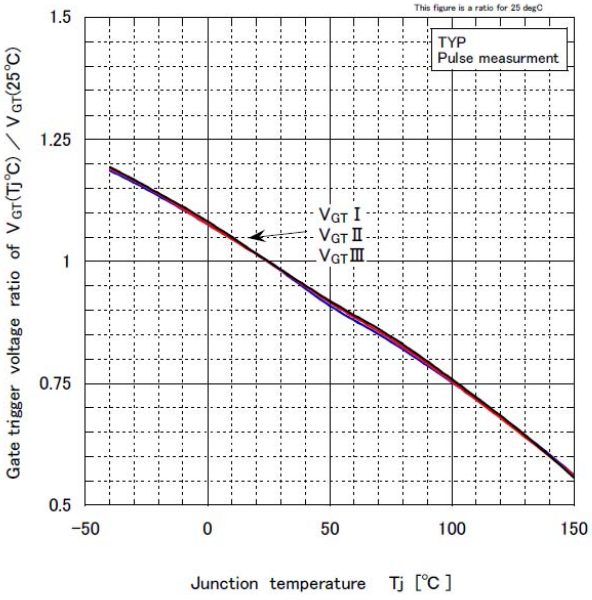
Gate trigger current vs Pulse width



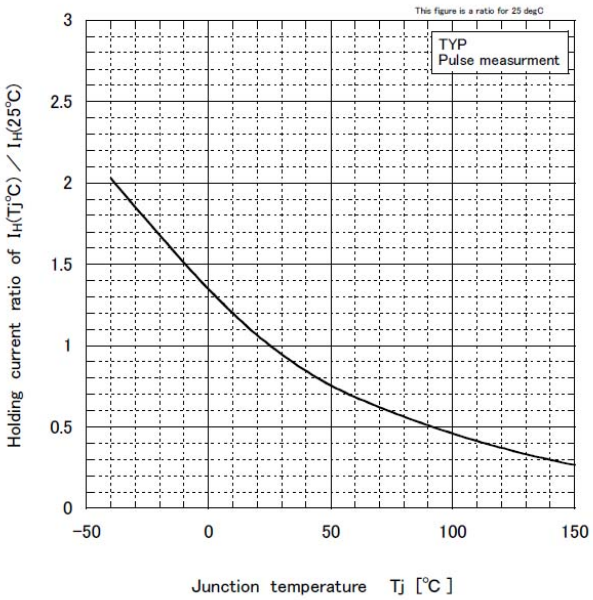
Gate trigger current vs Junction temperature



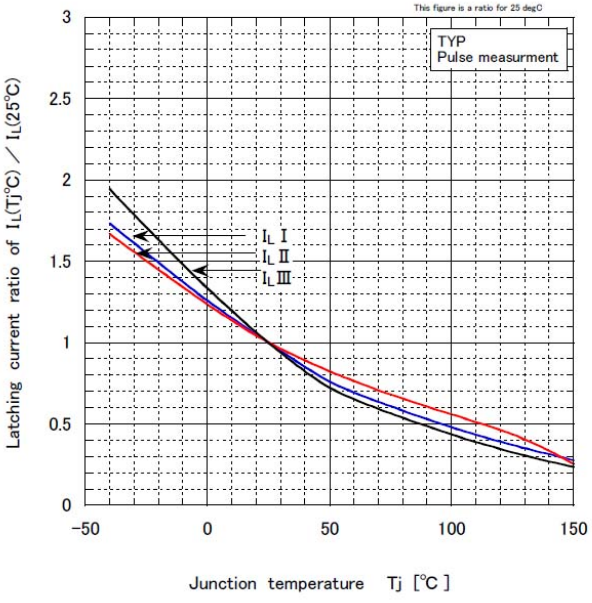
Gate trigger voltage vs Junction temperature

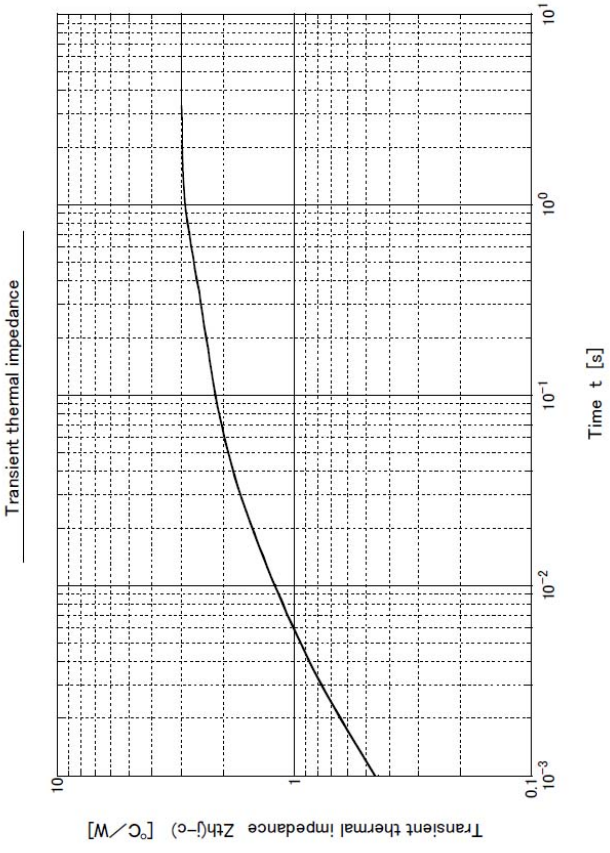


Holding current vs Junction temperature



Latching current vs Junction temperature





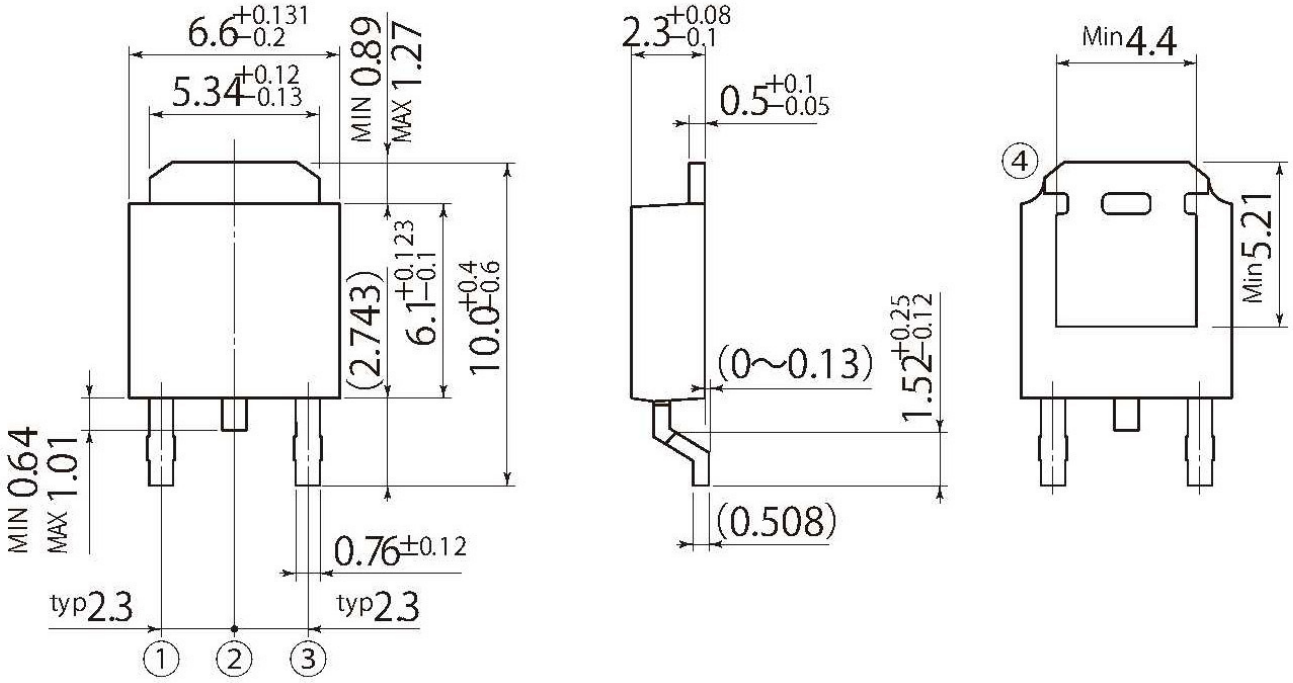
Outline Dimensions

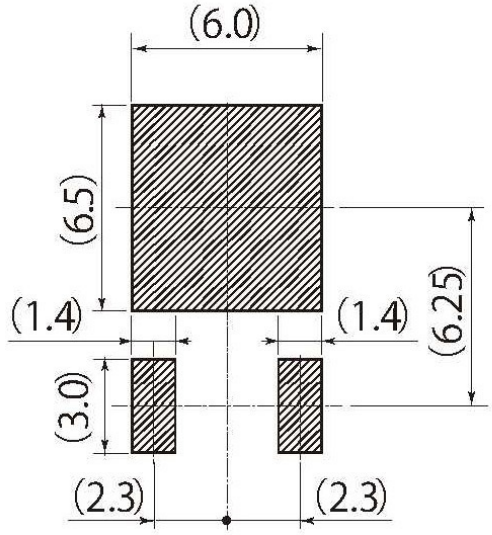
unit:mm

scale: 4/1

G2

JEDEC Code	TO-252AA
JEITA Code	–
House Name	FB





Referential Soldering Pad

・Optimize soldering pad to the board design and soldering condition.

Notes

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