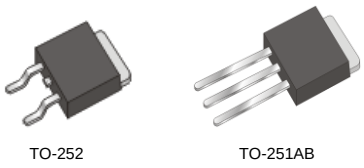
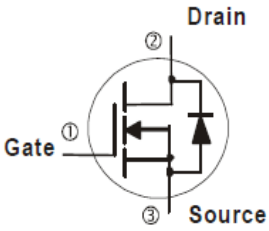
PJU45N06A / PJD45N06A

60V N-Channel Enhancement Mode MOSFET

Voltage	60 V	Current	45 A





Features

- $R_{DS(ON)}$, V_{GS}@10V,I_D@30A<12mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V,I_D@15A<15mΩ
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case : TO-251AB , TO-252 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-251AB Approx. Weight : 0.0104 ounces, 0.297grams
- TO-252 Approx. Weight : 0.0104 ounces, 0.297grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C=25°C	I_D	45	A
	T_C=100°C		29	
Pulsed Drain Current	T_C=25°C	I_{DM}	180	
Power Dissipation	T_C=25°C	P_D	63	W
	T_C=100°C		25	
Continuous Drain Current	T_A=25°C	I_D	9.5	A
	T_A=70°C		7.6	A
Power Dissipation	T_A=25°C	P_D	2.5	W
Power Dissipation	T_A=70°C		1.6	
Single Pulse Avalanche Energy (Note 1)		E_{AS}	61	mJ
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance	Junction to Case	$R_{\theta JC}$	2.0	°C/W
	Junction to Ambient	$R_{\theta JA}$	50	

- Limited only By Maximum Junction Temperature

PJU45N06A / PJD45N06A

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V,I_D=250uA	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	1.0	1.7	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=30A	-	10.5	12	mΩ
		V_{GS}=4.5V,I_D=15A	-	12	15	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V,V_{GS}=0V	-	0.01	1.0	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	±10	±100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_g	V_{DS}=30V, I_D=10A, V_{GS}=10V (Note 2,3)	-	39	-	nC
Gate-Source Charge	Q_{gs}		-	6.1	-	
Gate-Drain Charge	Q_{gd}		-	6.7	-	
Input Capacitance	Ciss	V_{DS}=25V, V_{GS}=0V, f=1.0MHZ	-	2256	-	pF
Output Capacitance	Coss		-	145	-	
Reverse Transfer Capacitance	Crss		-	93	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=15V, I_D=10A, V_{GS}=10V, R_G=6Ω (Note 2,3)	-	7.5	-	ns
Turn-On Rise Time	t_r		-	36	-	
Turn-Off Delay Time	$td_{(off)}$		-	49	-	
Turn-Off Fall Time	t_f		-	12	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	45	A
Diode Forward Voltage	V_{SD}	I_S=1A,V_{GS}=0V	-	0.67	1.0	V

NOTES :

1. The test by surface mounted on 1 inch FR4 board with 2oz copper.
2. L=0.1mH, I_{AS}=35A, V_{DD}=25V, V_{GS}=10V, R_G=25ohm, Starting T_J=25°C
3. The Power dissipation is limit by 150℃ junction temperature.
4. Pulse width≤300us, Duty cycle≤2%
5. Guaranteed by design, not subject to production testing

PJU45N06A / PJD45N06A

TYPICAL CHARACTERISTIC CURVES

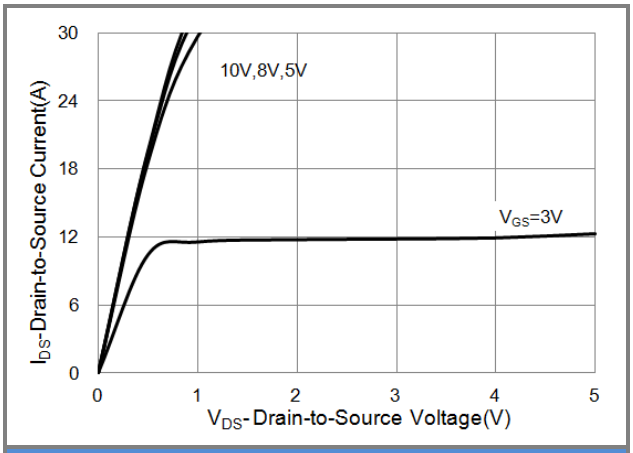


Fig.1 Output Characteristics

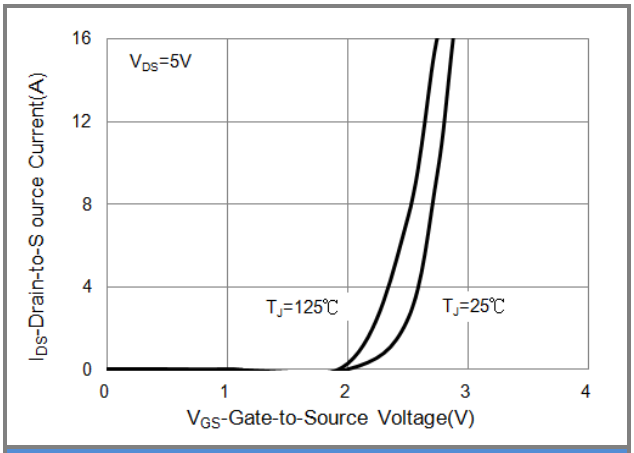


Fig.2 Transfer Characteristics

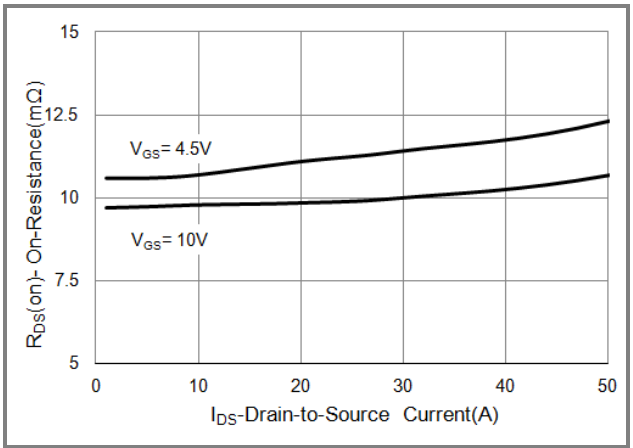


Fig.3 On-Resistance vs. Drain Current

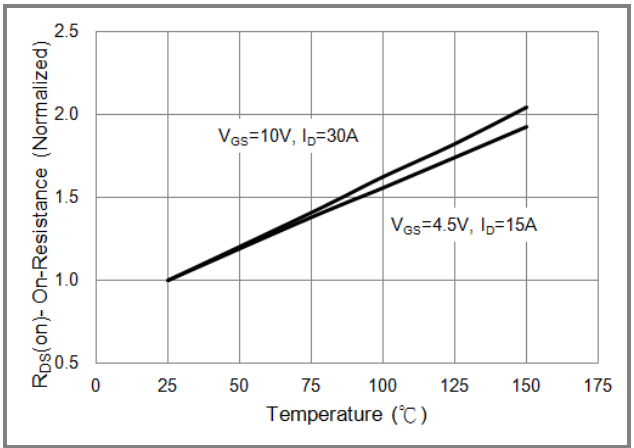


Fig.4 On-Resistsnce vs. Junction temperature

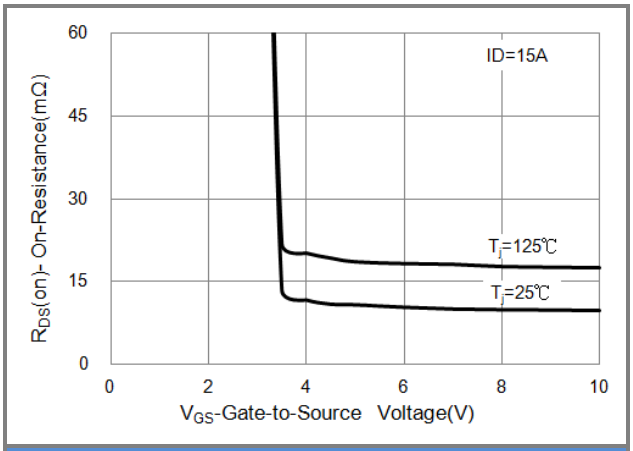


Fig.5 On-Resistance Variation with VGS.

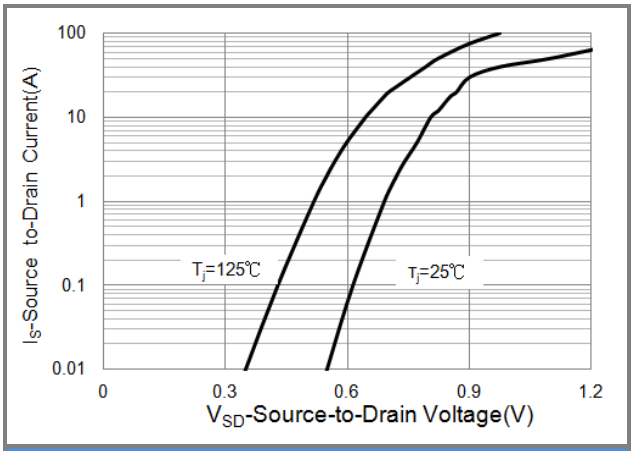


Fig.6 Source-Drain Diode Forward Voltage

PJU45N06A / PJD45N06A

TYPICAL CHARACTERISTIC CURVES

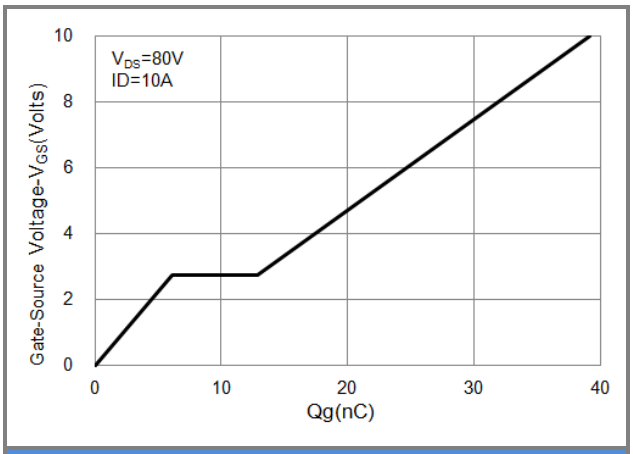


Fig.7 Gate-Charge Characteristics

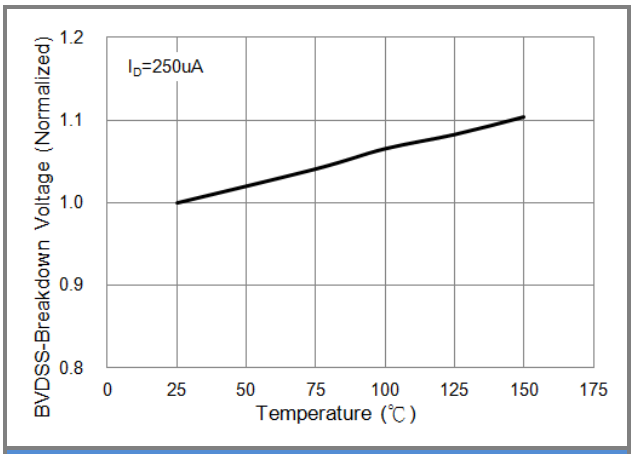


Fig.8 Breakdown Voltage Variation vs. Temperature

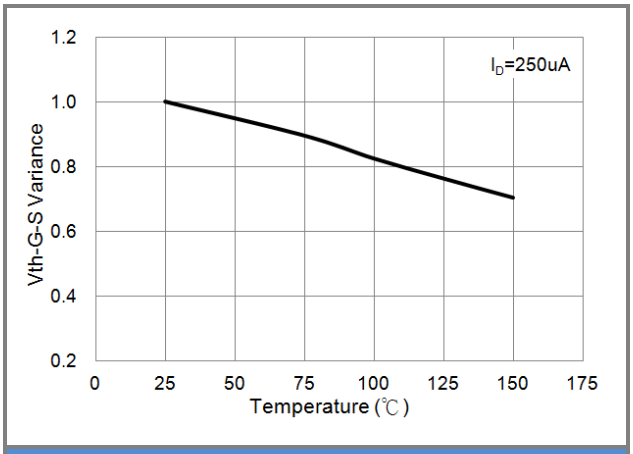


Fig.9 Threshold Voltage Variation with Temperature

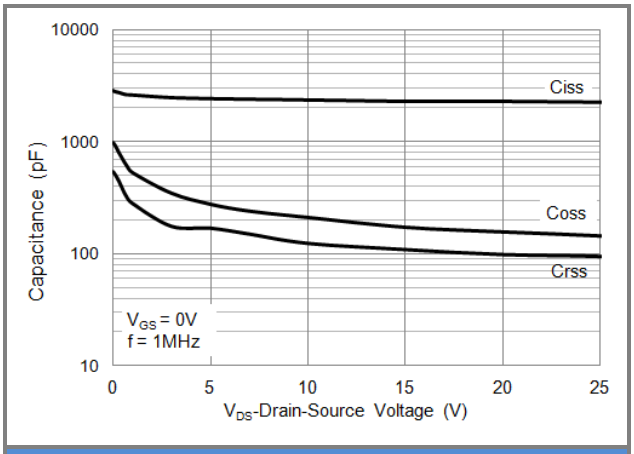


Fig.10 Capacitance vs. Drain-Source Voltage

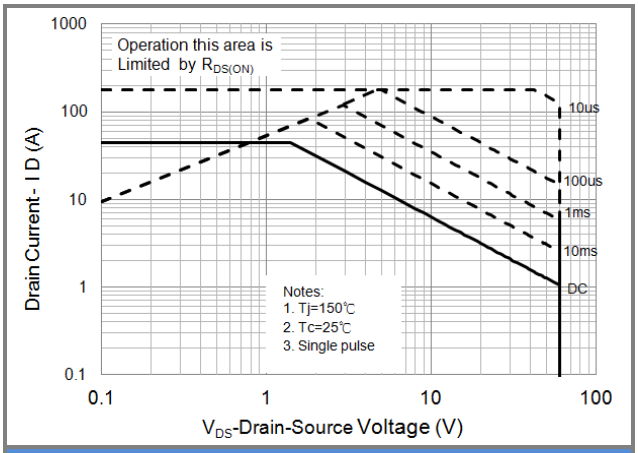


Fig.11 Maximum Safe Operating Area

PJU45N06A / PJD45N06A

TYPICAL CHARACTERISTIC CURVES

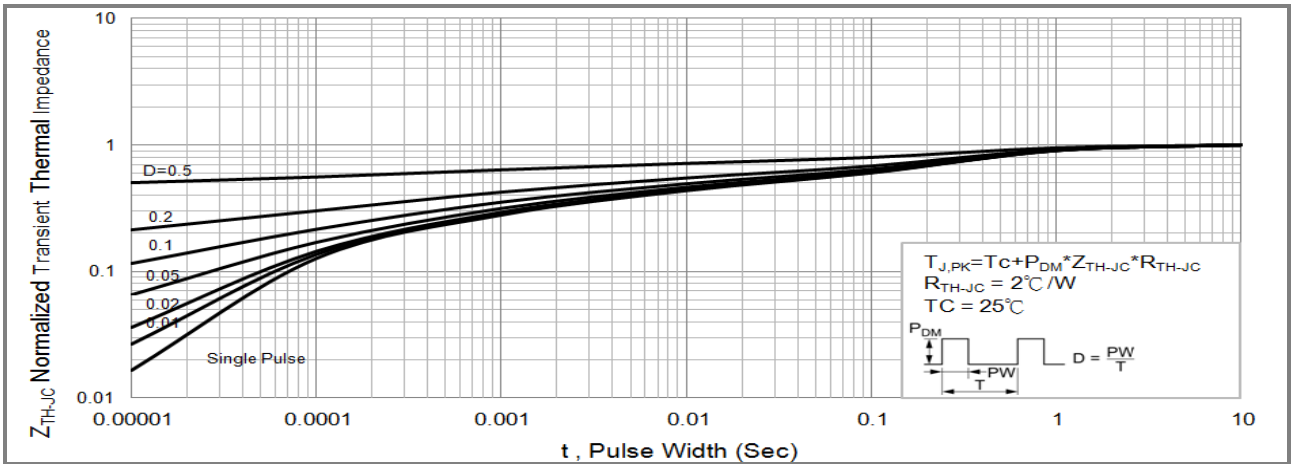


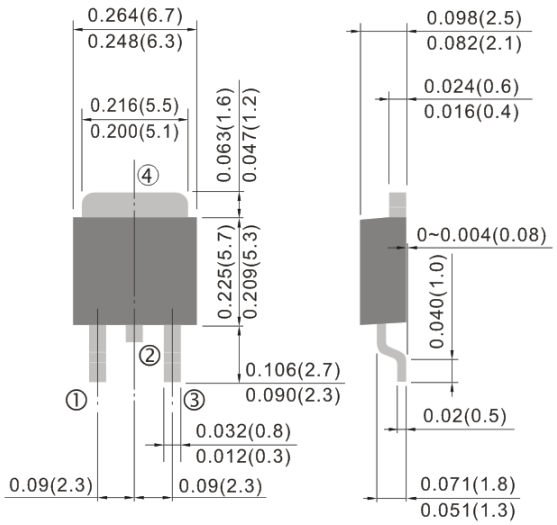
Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

PJU45N06A / PJD45N06A

Packaging Information .

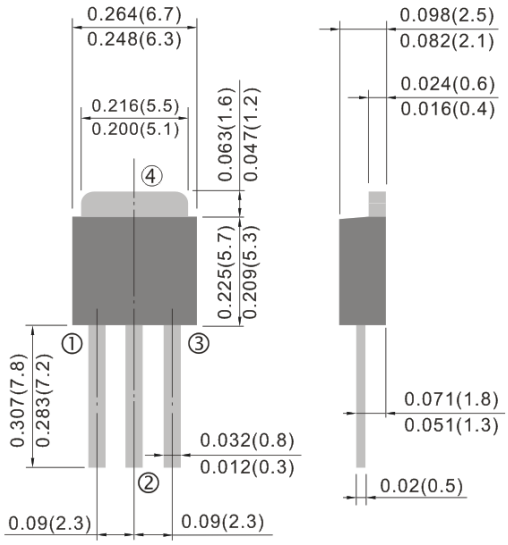
TO-252 Dimension

Unit: mm



TO-251AB Dimension

Unit: mm

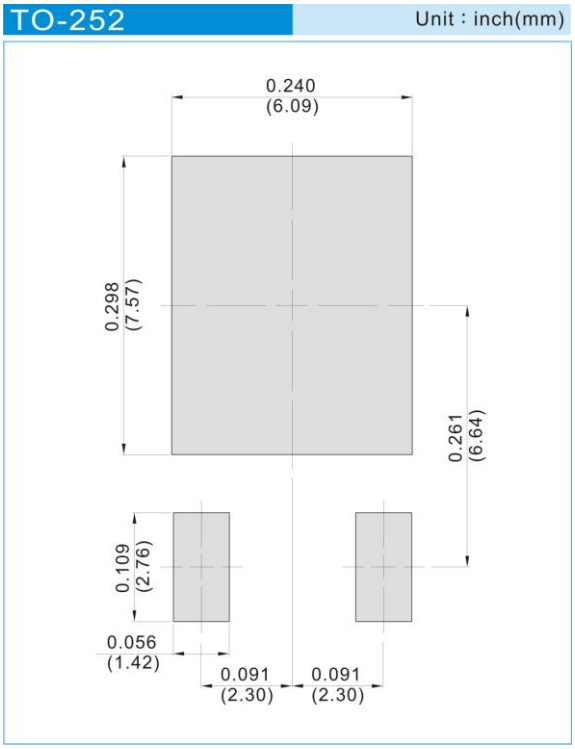


PJU45N06A / PJD45N06A

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD45N06A_L2_00001	TO-252	3,000pcs / 13” reel	D45N06A	Halogen free
PJU45N06A_T0_00001	TO-251AB	80pcs / Tube	U45N06A	Halogen free

MOUNTING PAD LAYOUT



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