P25B6EB

Power MOSFETs
60V, 25A, N-channel

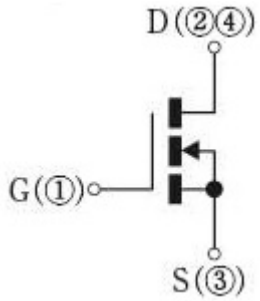
Feature

- N-channel
- SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FB
Package (JEDEC Code): TO-252AA

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	℃
Channel tempertature	Tch		-55 to 150	℃
Drain-source voltage	V_{DSS}		60	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I_D		25	A
Continuous drain current(Peak)	I_{DP}	Pulse width 10μs, duty=1/100	70	A
Total power dissipation	P_T		35	W
Single avalanche current	I_{AS}	Starting Tch=25℃ Tch≦150℃	16	A
Single avalanche energy	E_{AS}	Starting Tch=25℃ Tch≦150℃	29	mJ

※ :See the original Specifications

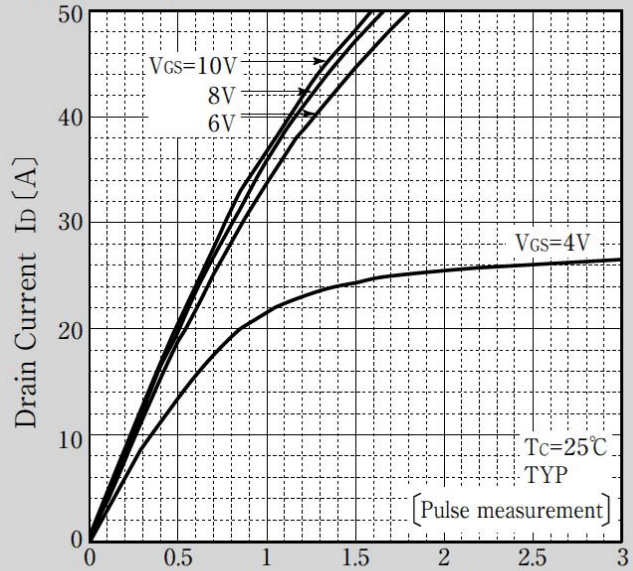
Electrical Characteristics (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	60			V
Zero gate voltage drain current	I_{DSS}	VDS=60V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±10	μA
Forward transconductance	g_{fs}	ID=12.5A, VDS=10V	6	12		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=12.5A, VGS=10V		0.023	0.029	Ω
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=12.5A, VGS=4.5V		0.0295	0.04	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V_{SD}	IS=25A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case, with heatsink ※			3.55	℃/W
Total gate charge	Qg	VDD=48V, VGS=10V, ID=25A		14.5		nC
Gate to source charge	Qgs	VDD=48V, VGS=10V, ID=25A		3.3		nC
Gate to drain charge	Qgd	VDD=48V, VGS=10V, ID=25A		4.5		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		785		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		45		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		115		pF
Turn-on delay time	td(on)	ID=12.5A, RL=2.4Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		18		ns
Rise time	tr	ID=12.5A, RL=2.4Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		110		ns
Turn-off delay time	td(off)	ID=12.5A, RL=2.4Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		55		ns
Fall time	tf	ID=12.5A, RL=2.4Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		80		ns

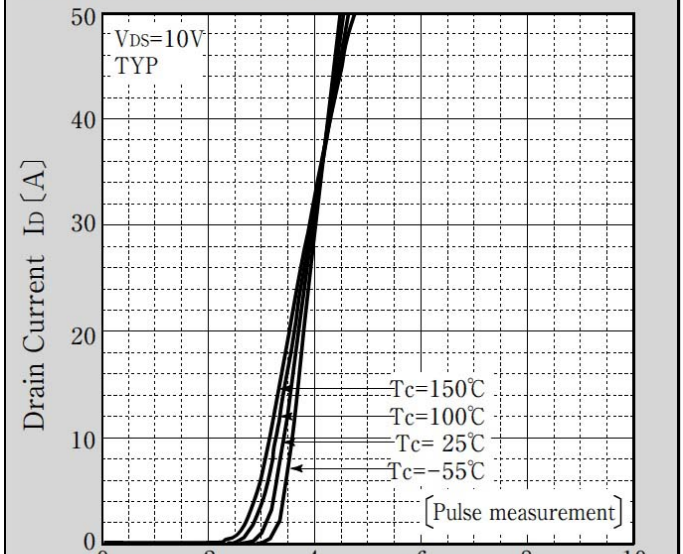
※ ：See the original Specifications

CHARACTERISTIC DIAGRAMS

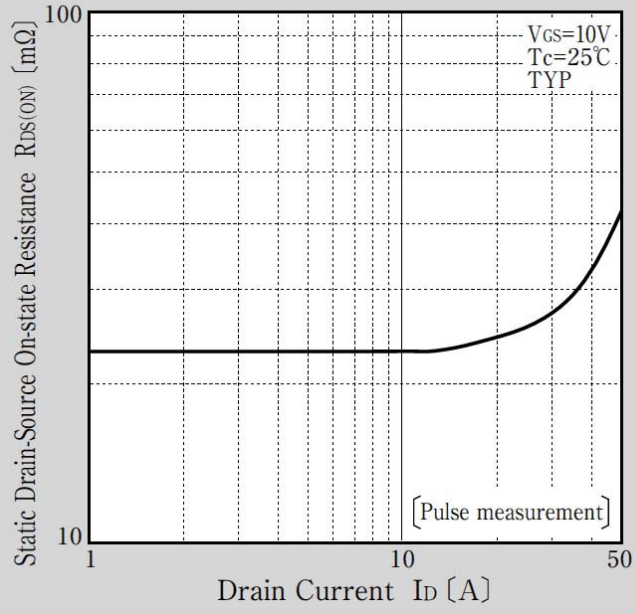
Typical Output Characteristics



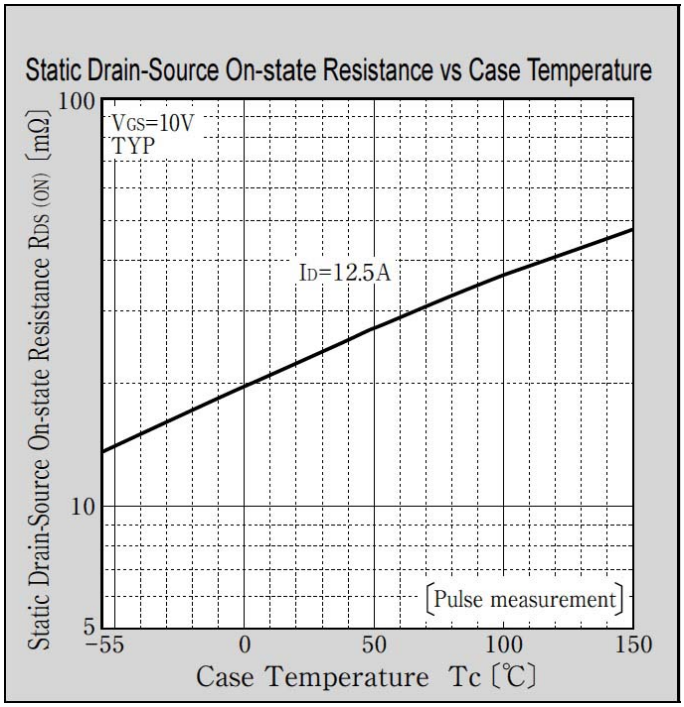
Transfer Characteristics



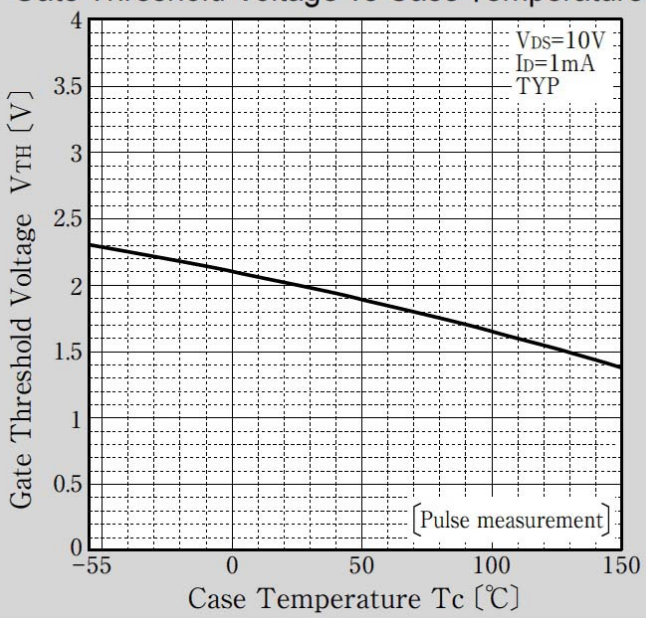
Static Drain-Source On-state Resistance vs Drain Current



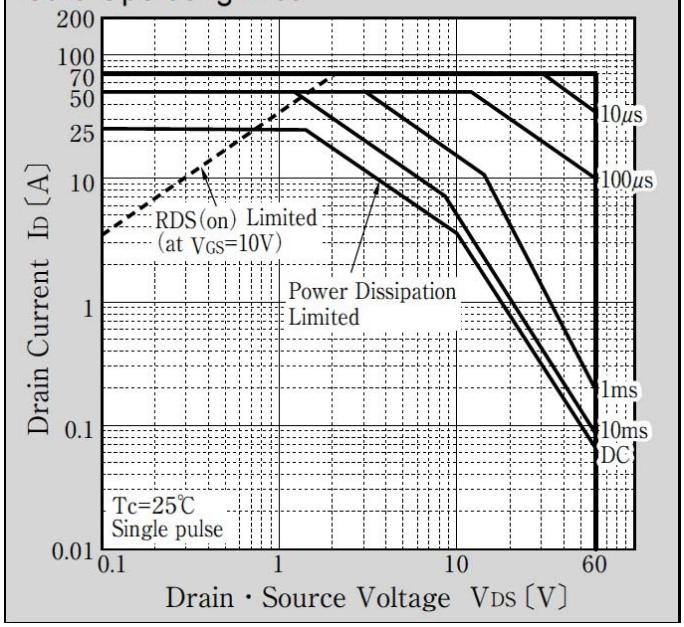
Static Drain-Source On-state Resistance vs Case Temperature



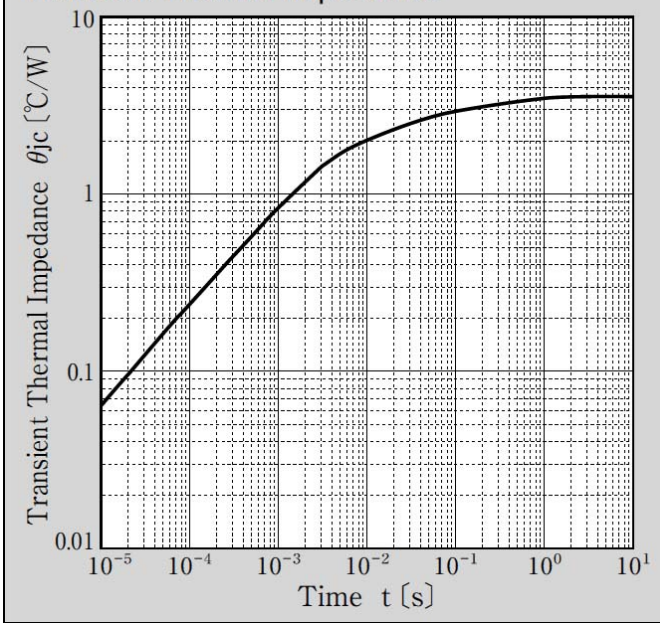
Gate Threshold Voltage vs Case Temperature



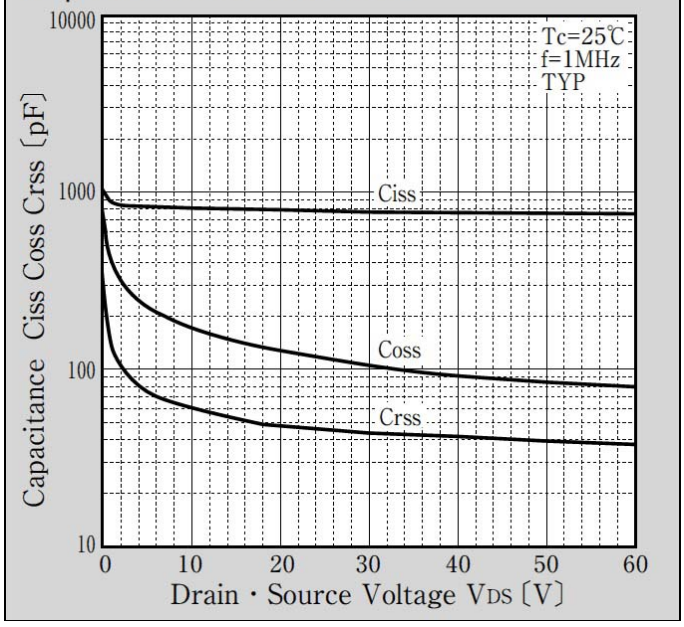
Safe Operating Area

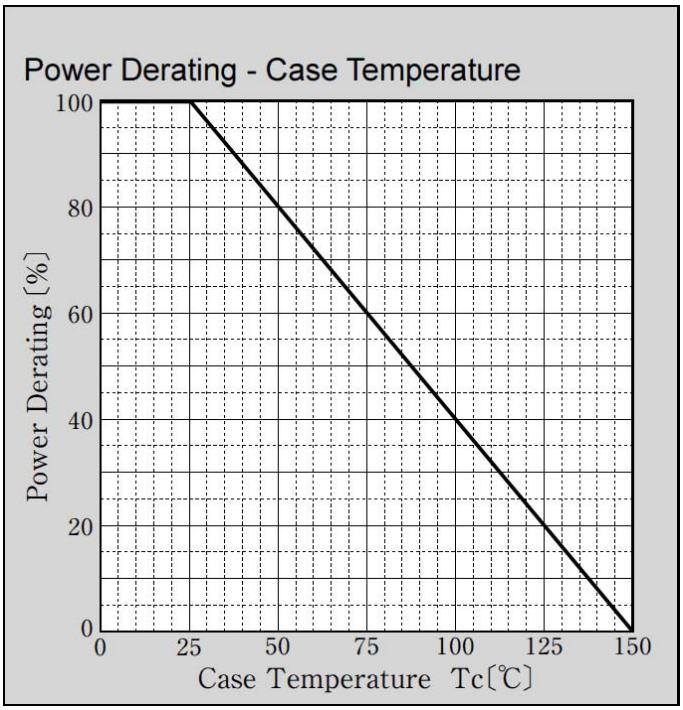


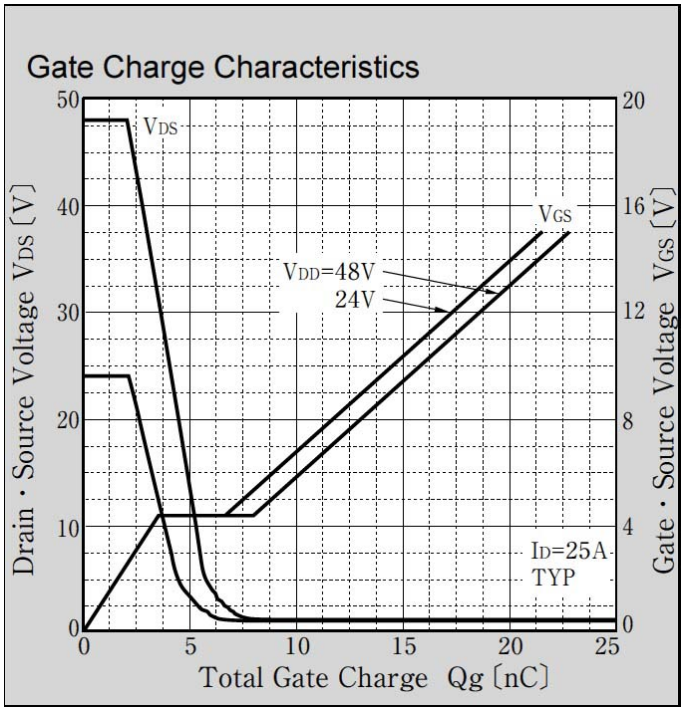
Transient Thermal Impedance

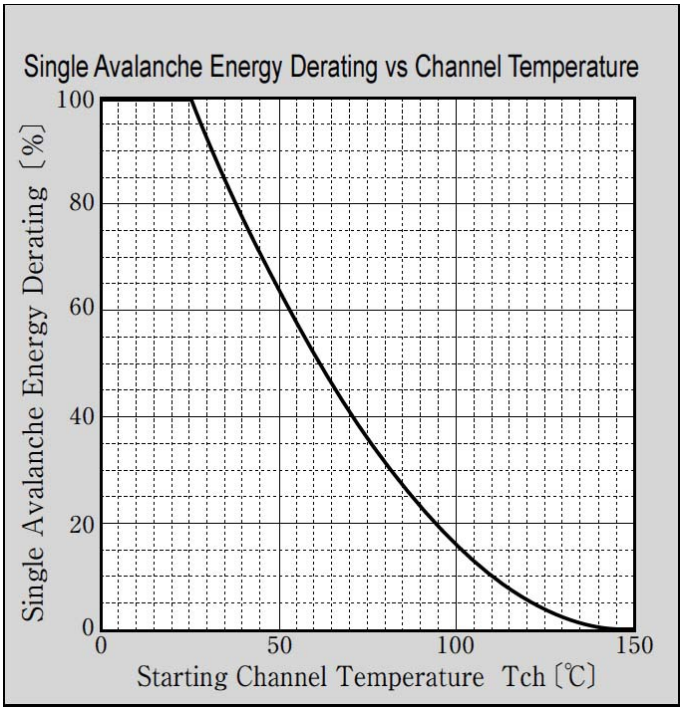


Capacitance Characteristics




Power Derating - Case Temperature
Power Derating〔%〕
Case Temperature Tc〔℃〕
0
20
40
60
80
100
0
25
50
75
100
125
150


Gate Charge Characteristics
Drain・Source Voltage VDS〔V〕
Gate・Source Voltage VGS〔V〕
Total Gate Charge Qg〔nC〕
VDS
VGS
VDD=48V
24V
ID=25A
TYP
0
10
20
30
40
50
0
4
8
12
16
20
0
5
10
15
20
25


Single Avalanche Energy Derating vs Channel Temperature
Single Avalanche Energy Derating〔%〕
Starting Channel Temperature Tch〔℃〕
0
20
40
60
80
100
0
50
100
150

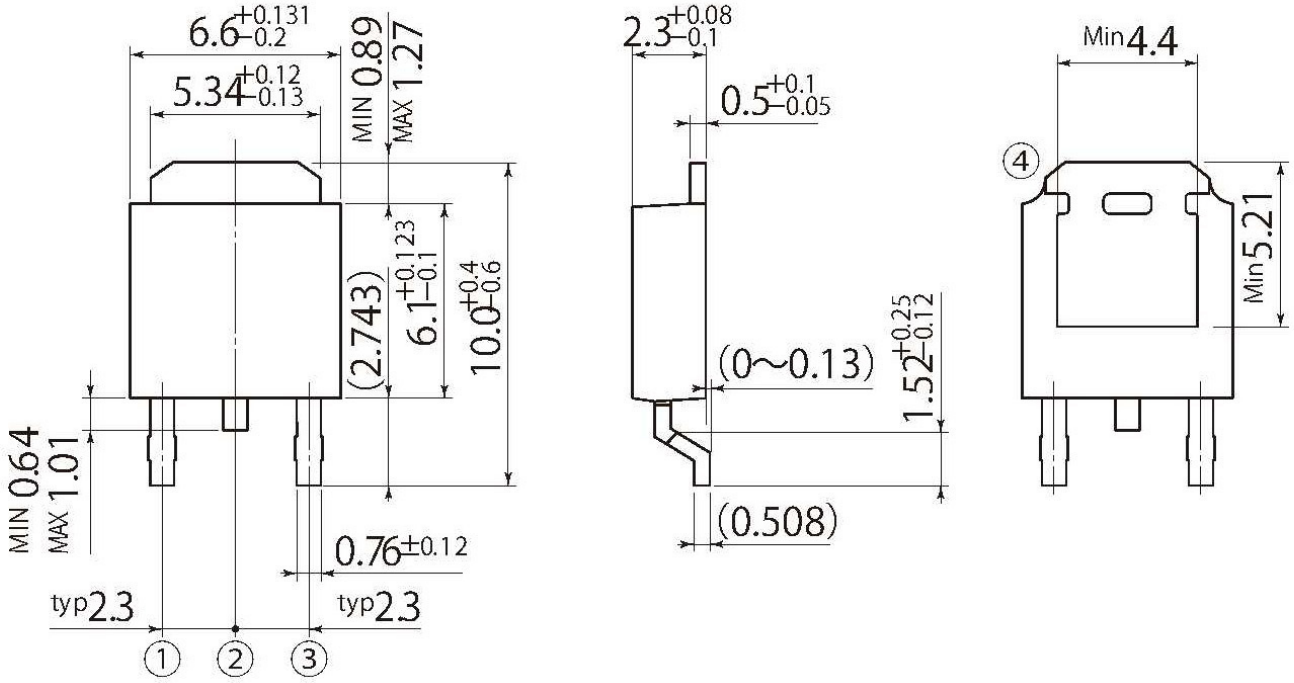
Outline Dimensions

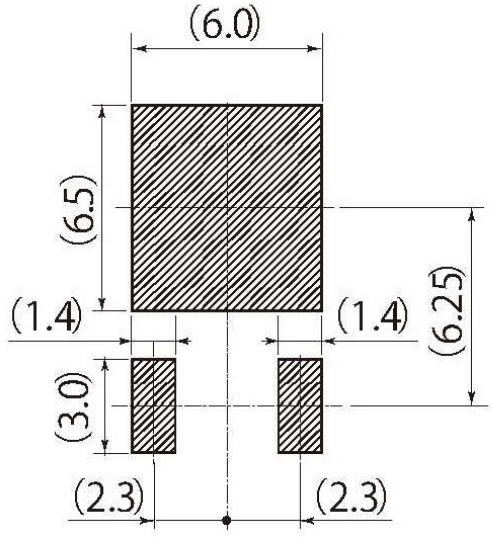
unit:mm

scale: 4/1

G2

JEDEC Code	TO-252AA
JEITA Code	–
House Name	FB





Referential Soldering Pad

・Optimize soldering pad to the board design and soldering condition.

Notes

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