

P9B40HP2

Power MOSFETs

400V, 9A, N-channel

Feature

- N-channel
- SMD
- High Voltage
- Low Capacitance
- High Avalanche Durability, High di/dt Durability
- Pb free terminal
- RoHS:Yes

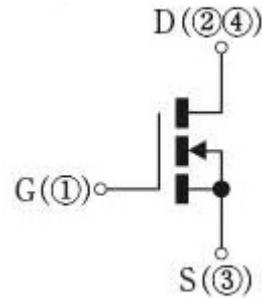
OUTLINE

Package (House Name): FB

Package (JEDEC Code): TO-252AA



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Channel temperature	T _{ch}		150	°C
Drain-source voltage	V _{DSS}		400	V
Gate-source voltage	V _{GSS}		±30	V
Continuous drain current(DC)	I _D		9	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	36	A
Continuous source current(DC)	I _S		9	A
Total power dissipation	P _T		40	W
Repetitive avalanche current	I _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	9	A
Single avalanche energy	E _{AS}	Starting T _{ch} =25°C T _{ch} ≤150°C	37	mJ
Repetitive avalanche energy	E _{AR}	Starting T _{ch} =25°C T _{ch} ≤150°C	3.7	mJ
Drain-source diode di/dt strength	di/dt	I _S =9A, T _c =25°C	350	A/μs

* :See the original Specifications

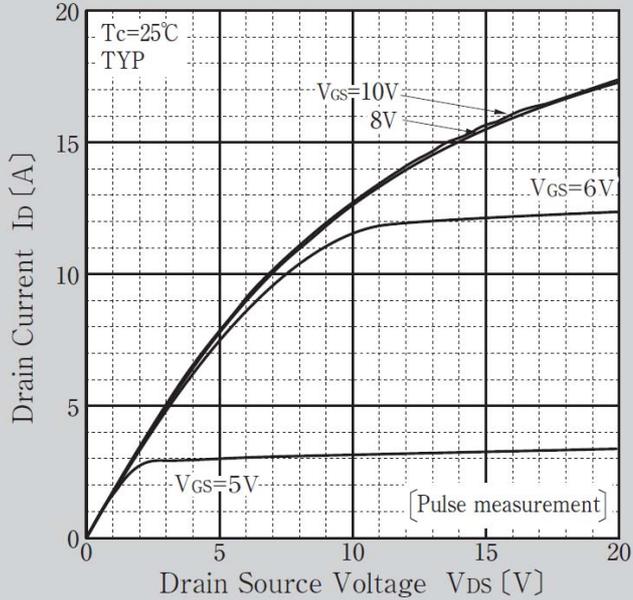
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	400			V
Zero gate voltage drain current	I_{DSS}	VDS=400V, VGS=0V			100	μA
Gate-source leakage current	I_{GSS}	VGS=±25V, VDS=0V			±10	μA
Forward transconductance	g_{fs}	ID=4.5A, VDS=10V	3.5	7		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=4.5A, VGS=10V		0.65	0.8	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	3	3.75	4.5	V
Source-drain diode forward voltage	V_{SD}	IS=4.5A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case			3.12	°C/W
Total gate charge	Q_g	VDD=320V, VGS=10V, ID=9A		14.5		nC
Input capacitance	C_{iss}	VDS=50V, VGS=0V, f=1MHz		575		pF
Reverse transfer capacitance	C_{rss}	VDS=50V, VGS=0V, f=1MHz		5		pF
Output capacitance	C_{oss}	VDS=50V, VGS=0V, f=1MHz		60		pF
Turn-on delay time	$t_{d(on)}$	ID=4.5A, RL=33.3Ω, VDD=150V, VGS(+)=10V, VGS(-)=0V		8.5		ns
Rise time	t_r	ID=4.5A, RL=33.3Ω, VDD=150V, VGS(+)=10V, VGS(-)=0V		30		ns
Turn-off delay time	$t_{d(off)}$	ID=4.5A, RL=33.3Ω, VDD=150V, VGS(+)=10V, VGS(-)=0V		50		ns
Fall time	t_f	ID=4.5A, RL=33.3Ω, VDD=150V, VGS(+)=10V, VGS(-)=0V		25		ns

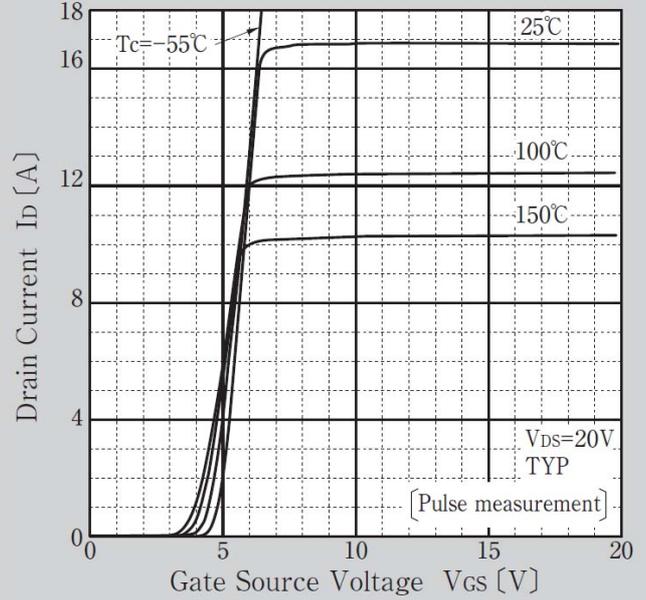
* : See the original Specifications

CHARACTERISTIC DIAGRAMS

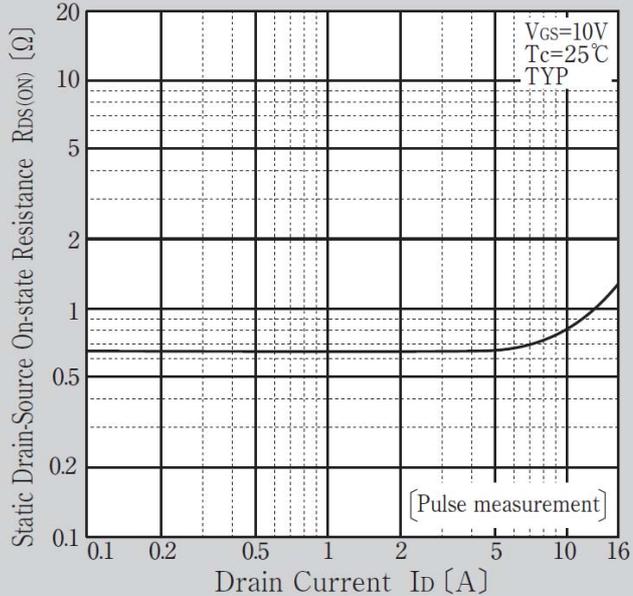
Typical Output Characteristics



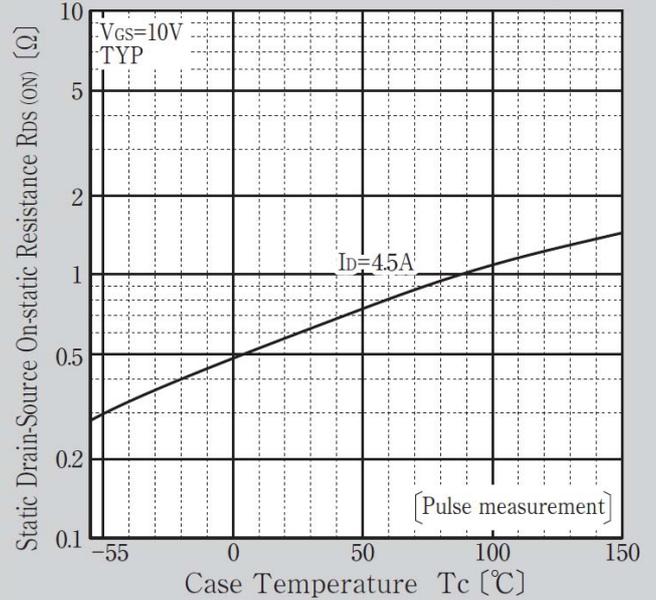
Transfer Characteristics



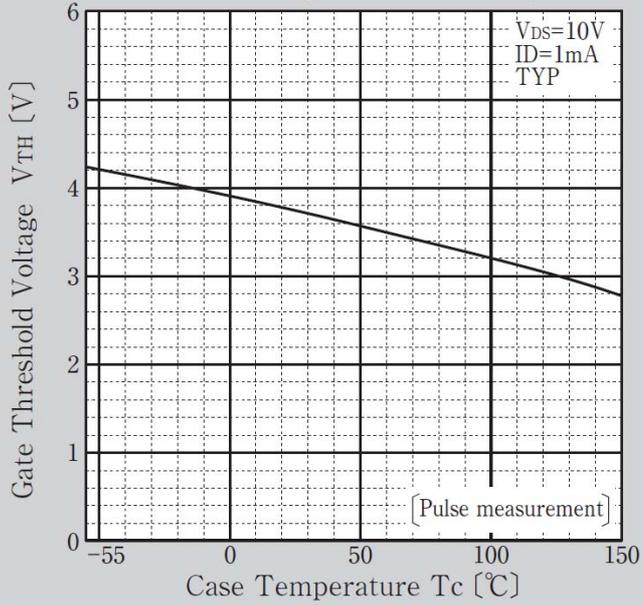
Static Drain-Source On-state Resistance vs Drain Current



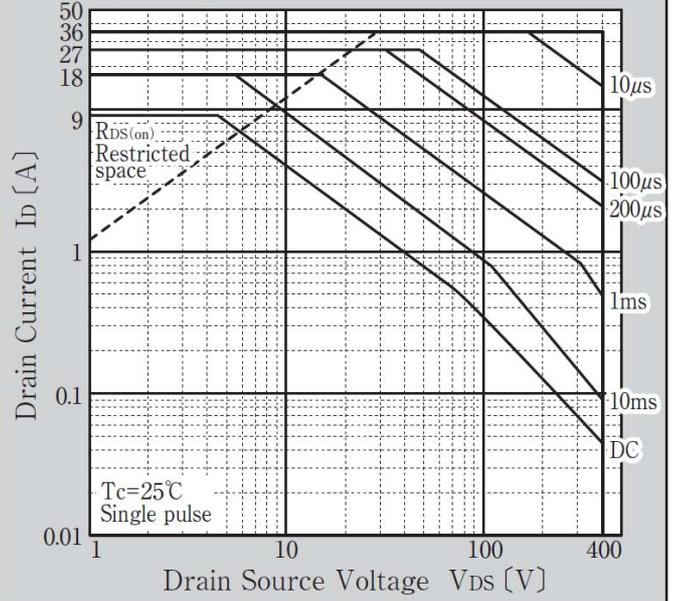
Static Drain-Source On-state Resistance vs Case Temperature



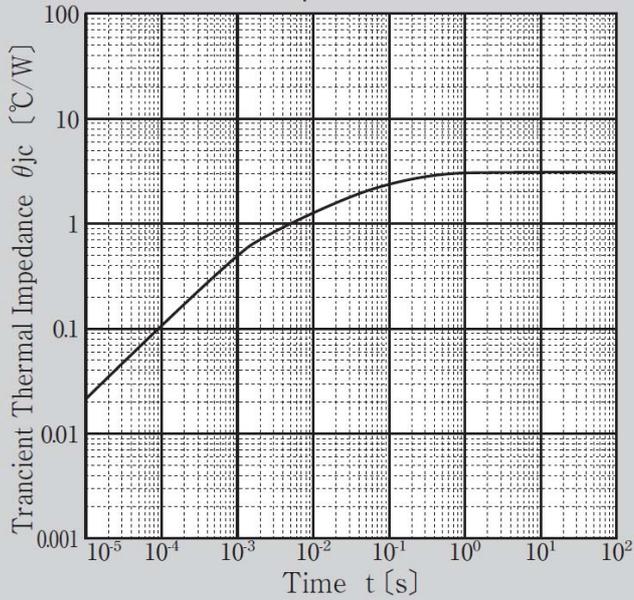
Gate Threshold Voltage vs Case Temperature



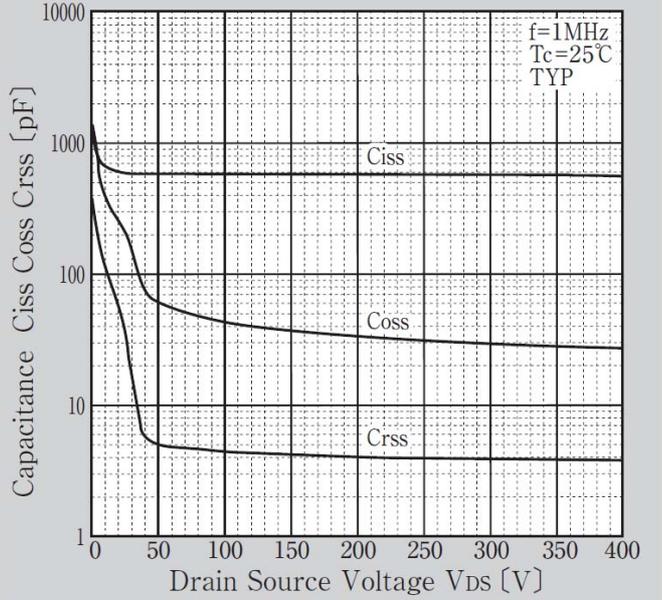
Safe Operating Area



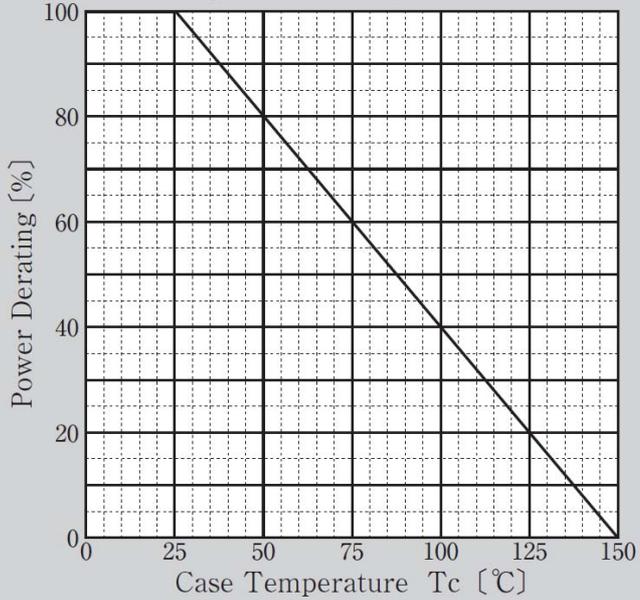
Transient Thermal Impedance



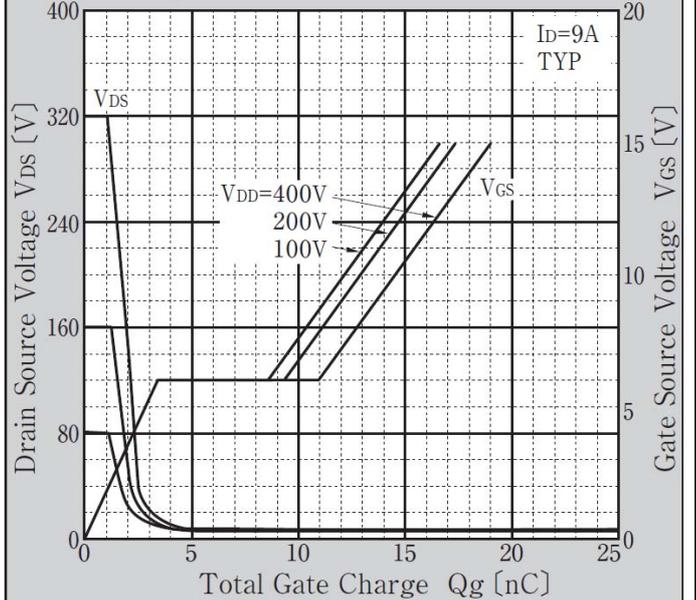
Capacitance Characteristics



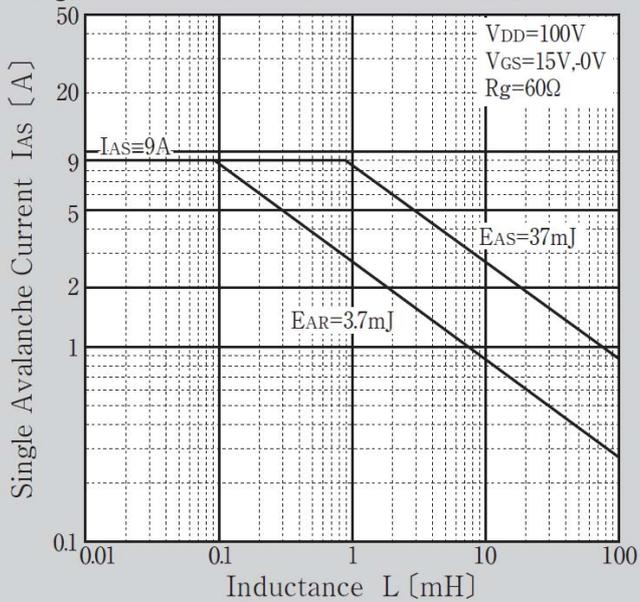
Power Derating - Case Temperature



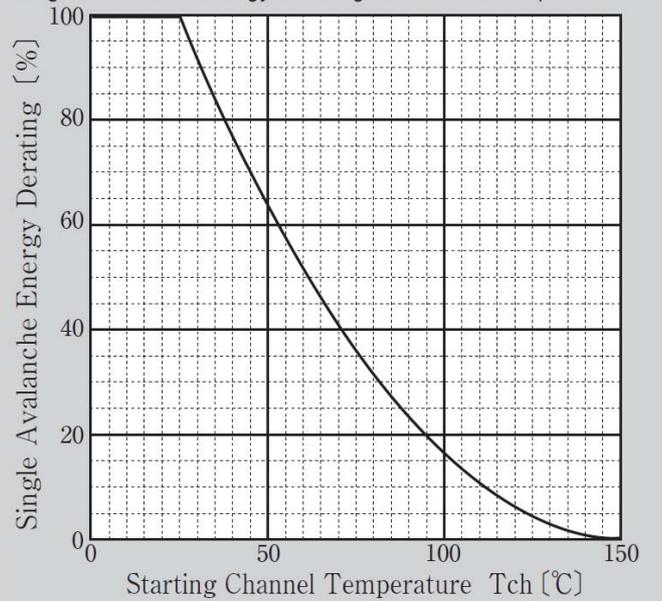
Gate Charge Characteristics



Single Avalanche Current vs Inductive Load

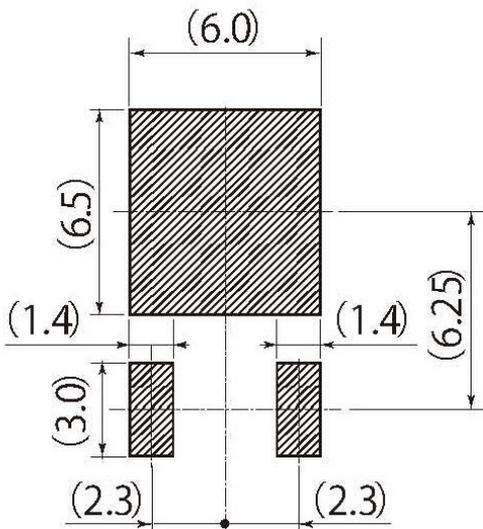
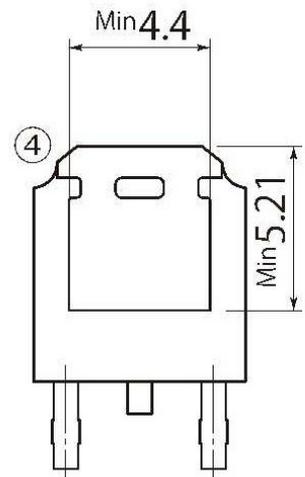
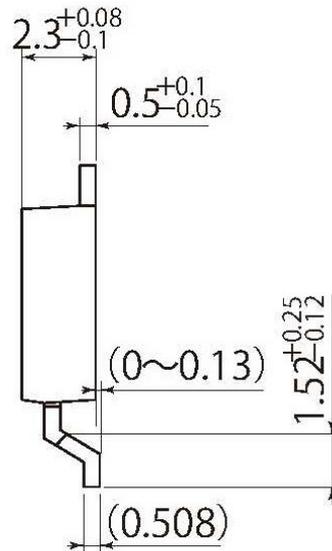
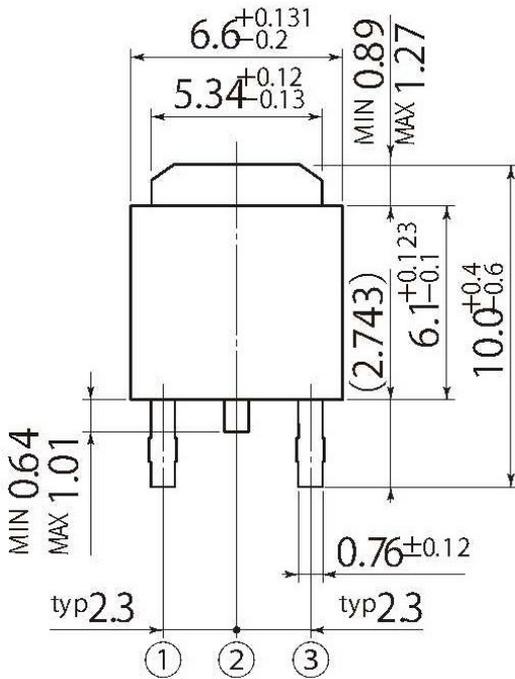


Single Avalanche Energy Derating vs Channel Temperature



G2

JEDEC Code	TO-252AA
JEITA Code	-
House Name	FB



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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