



5-TVS/ZENER ARRAY FOR ESD AND LATCH-UP PROTECTION

This 5-TVS/Zener Array has been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in CMOS circuitry, operating at 5V,as well available for 12V,15V, and 24V Systems. This TVS array offers an integrated solution to protect up to 5 data lines where the board space is a premium.

FEATURES

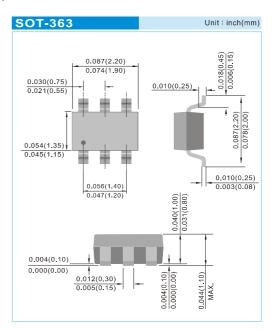
- 100W Power Dissipation (8/20µs Waveform)
- · Low Leakage Current
- · Very Low Clamping Voltage
- IEC61000-4-2 ESD 20kV air,15kV Contact Compliance
- · Operating voltage options for 5V,12V,15V, and 24V
- · Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. . (Halogen Free)

MECHANICAL DATA

- · Case: SOT-363. Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- · Appox Weight: 0.0002 ounces, 0.006 grams.

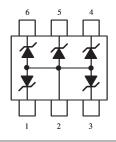
APPLICATIONS

- · Personal Digital Assistant (PDA)
- · SIM Card Port Protection (Mobile Phone)
- · Portable Instrumentation
- · Mobile Phones and Accessories
- · Memory Card Port Protection



MAXIMUM RATINGS (Per Device)

RATING	SYMBOL	VALUE	UNITS
Peak Pulse Power (8/20μs Waveform)	Ppp	100	W
ESD Voltage (HBM)	VESD	25	kV
Operating Temperature Range	TJ	-55 to 125	°C
Storage Temperature Range	Тѕтѕ	-55 to 150	°C







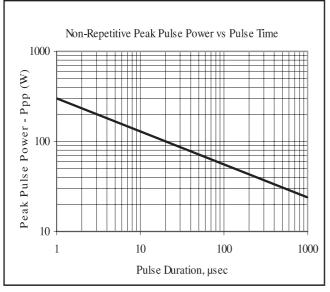
ELECTRICAL CHARACTERISTICS@TJ=25°C (Per Device)

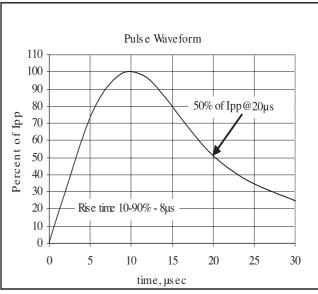
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	Vwrm	-	-	-	5	V
Reverse Breakdown Voltage	VBR	I BR=1mA	6.2	-	-	V
Reverse Leakage Current	I R	V _R =5V	-	-	0.5	μΑ
Clamping Voltage (8/20μs)	Vc	I pp=5A	-	-	10	V
Clamping Voltage (8/20μs)	Vc	I pp=9A	-	-	11	V
Off State Junction Capacitance	Cı	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	100	pF
PJCF12LC Marking C12						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	Vwrm	-	-	-	12	V
Reverse Breakdown Voltage	VBR	I BR=1mA	13.3	-	-	V
Reverse Leakage Current	l R	V _R =12V	-	-	0.5	μА
Clamping Voltage (8/20μs)	Vc	I pp=3A	-	-	18	V
Clamping Voltage (8/20μs)	Vc	I pp=5A	-	-	20	V
Off State Junction Capacitance	Cı	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	50	pF
PJCF15LC Marking C15						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	Vwrm	-	-	-	15	V
Reverse Breakdown Voltage	VBR	I BR=1mA	16.6	-	-	V
Reverse Leakage Current	I R	V _R =15V	-	-	0.5	μА
Clamping Voltage (8/20μs)	Vc	I PP=3A	-	-	23	V
Clamping Voltage (8/20μs)	Vc	I PP=4A	-	-	25	V
Off State Junction Capacitance	CJ	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	40	pF
PJCF24LC Marking C24	-					
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	Vwrm	-	-	-	24	V
Reverse Breakdown Voltage	VBR	I BR=1mA	26.7	-	-	V
Reverse Leakage Current	I R	V _R =24V	-	-	0.5	μА
Clamping Voltage (8/20μs)	Vc	I pp=1A	-	-	35	V
Clamping Voltage (8/20μs)	Vc	I PP=2A	-	-	45	V
Off State Junction Capacitance	CJ	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	30	pF





TYPICAL CHARACTERISTICS 25°C unless otherwise noted

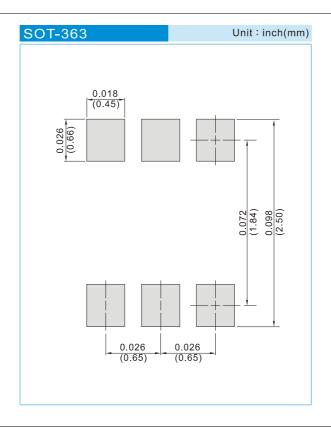








MOUNTING PAD LAYOUT



ORDER INFORMATION

• Packing information

T/R - 10K per 13" plastic Reel

T/R - 3K per 7" plastic Reel





Part No_packing code_Version

PJCF05LC_R1_00001 PJCF05LC_R2_00001

For example:



Packing Code XX			Version Code XXXXX			
Packing type	1 st Code	Packing size code	2 nd Code	HF or RoHS	1 st Code	2 nd ~5 th Code
Tape and Ammunition Box (T/B)	A	N/A	0	HF	0	serial number
Tape and Reel (T/R)	R	7"	1	RoHS	1	serial number
Bulk Packing (B/P)	В	13"	2			
Tube Packing (T/P)	Т	26mm	X			
Tape and Reel (Right Oriented) (TRR)	S	52mm	Y			
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP (PBCU)	U			
FORMING	F	PANASERT T/B CATHODE DOWN (PBCD)	D			





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