

Part Number: 2512067007Y3
 Frequency Range: Medium Current
 Description: MULTI-LAYER CHIP BEAD
 Application: Suppression Components
 Where Used: Board Component
 Part Type: Chip Beads
 Preferred Part: ✓

Mechanical Specifications

Weight: .030 (g)

Part Type Information

Fair-Rite offers a broad selection of cost effective multi-layer chip beads to suppress conducted EMI signals. Chip beads can be used in an array of devices such as cellular phones, computers, laptops, pagers, etc. The small package sizes accommodate automated placements and allow for a dense packaging of circuit boards. Chip beads are 100% tested for impedance and dc resistance. They are available in standard, high and GHz signal speeds. The multi-layer chip beads are organized by increasing package size and current carrying capacity.

- All multi-layer chip beads are supplied taped and reeled, if required bulk packed chip beads can be provided.
- The impedance values listed are typical values. The nominal impedance with a $\pm 25\%$ tolerance is specified for the + marked 100 MHz. Chip beads are measured for impedance on the HP 4291A and fixture HP 16192A.
- Chip beads have plated contacts, 100% tin over a nickel undercoating. They can accommodate both reflow and wave soldering technologies.
- The suggested land patterns are in accordance to the latest revision of IPC-7351.
- Recommended storage and operating temperature range is -55°C to 125°C.
- Our 'Chip Bead Kit' (part number 0199000018) is available for prototype evaluation.



Mechanical Specifications

| Dim | mm | mm tol | nominal inch | inch misc. |
|-----|------|-----------|-----------------|---------------|
| A | 1.10 | ±0.20 | 0.043 | - |
| B | 1.60 | ±0.20 | 0.063 | - |
| C | 3.20 | ±0.20 | 0.126 | - |
| D | 0.70 | ±0.30 | 0.028 | - |
| E | - | - | - | - |
| F | - | - | - | - |
| G | - | - | - | - |
| H | - | - | - | - |
| J | - | - | - | - |
| K | - | - | - | - |

Electrical Specifications

| Typical Impedance (Ω) | |
|--------------------------------|---------|
| 50 MHz | 53 |
| 100 MHz+ | 70 ±25% |
| 500 MHz | 102 |
| 1000 MHz | 103 |

| Electrical Properties | |
|-----------------------|----------|
| Signal Speed | Standard |
| Max DCR (Ω) | 0.04 |
| Max Current (mA) | 3000 |

Land Patterns

| V | W ref | X | Y | Z |
|----------------|----------------|----------------|----------------|--------|
| 1.200 0.047 | 2.800 0.110 | 1.800 0.071 | 1.600 0.063 | - - |

Winding Information

| Turns Tested | Wire Size | 1st Wire Length | 2nd Wire Length |
|-----------------|--------------|--------------------|--------------------|
| - | - | - | - |

Reel Information

| Tape Width mm | Pitch mm | Parts 7 " Reel | Parts 13 " Reel | Parts 14 " Reel |
|------------------|-------------|-------------------|--------------------|--------------------|
| 8 | 4 | 3000 | 10000 | - |

Package Size

| Pkg Size |
|----------------|
| 1206 (3216) |

Connector Plate

| # Holes | # Rows |
|---------|--------|
| - | - |

Legend

+ Test frequency

Preferred parts, the suggested choice for new designs, have shorter lead times and are more readily available.

The column H(Oe) gives for each bead the calculated dc bias field in oersted for 1 turn and 1 ampere direct current. The actual dc H field in the application is this value of H times the actual NI (ampere-turn) product. For the effect of the dc bias on the impedance of the bead material, see figures 18-23 in the application note How to choose Ferrite Components for EMI Suppression.

A ½ turn is defined as a single pass through a hole.

Σ l/A - Core Constant

A_e - Effective Cross-Sectional Area

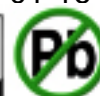
A_L - Inductance Factor ($\frac{L}{N^2}$)

N/AWG - Number of Turns/Wire Size for Test Coil

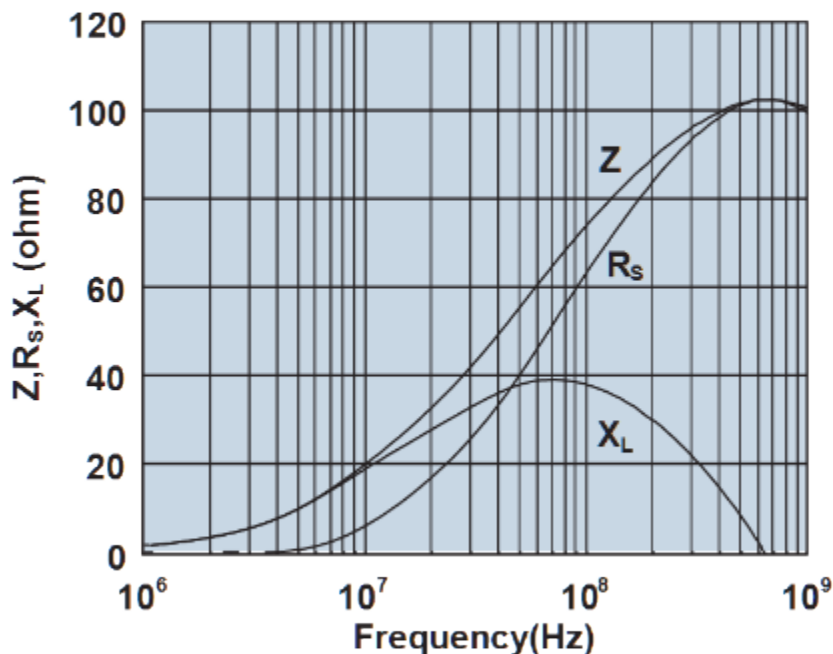
l_e - Effective Path Length

V_e - Effective Core Volume

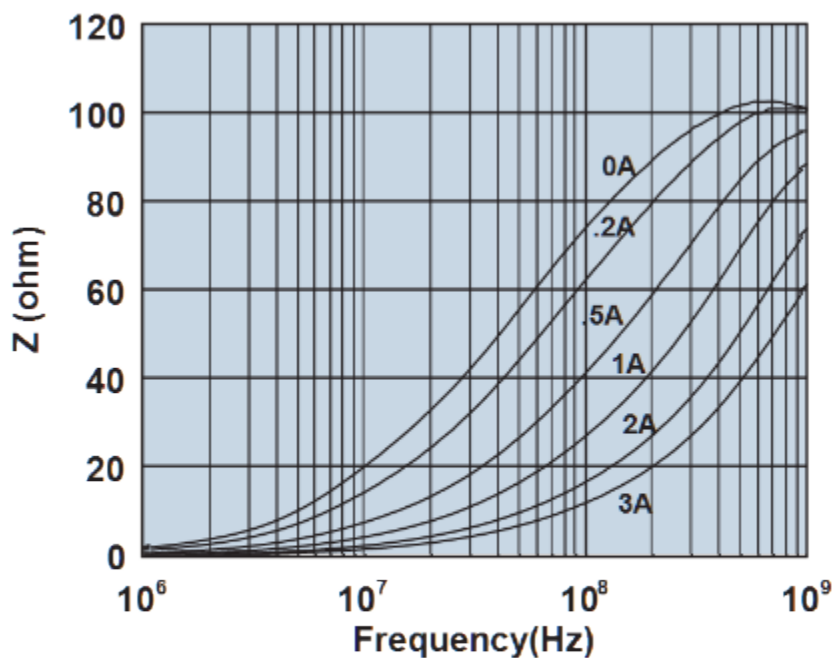
NI - Value of dc Ampere-turns



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Impedance, reactance, and resistance vs. frequency.



Impedance vs. frequency with dc bias.